

Modern SAT Solvers

Part B

Vienna Winter School on Verification

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TU Vienna, Austria

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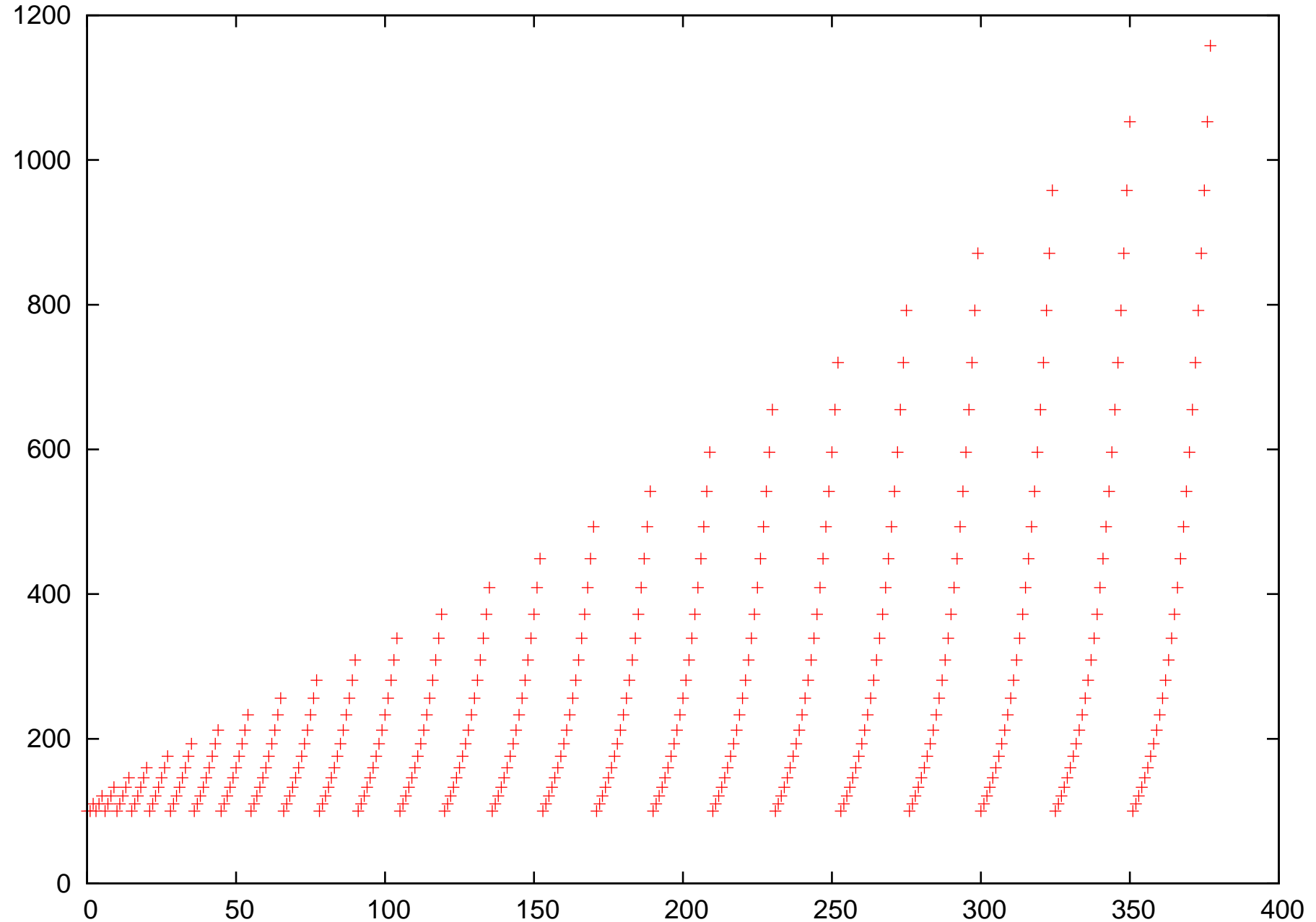
Institute for Formal Models and Verification

Johannes Kepler University, Linz, Austria

<http://fmv.jku.at>

- for satisfiable instances the solver may get stuck in the unsatisfiable part
 - even if the search space contains a large satisfiable part
- often it is a good strategy to abandon the current search and restart
 - restart after the number of decisions reached a *restart limit*
- avoid to run into the same dead end
 - by randomization (either on the decision variable or its phase)
 - and/or just keep all the learned clauses
- for completeness dynamically increase restart limit

378 restarts in 104408 conflicts



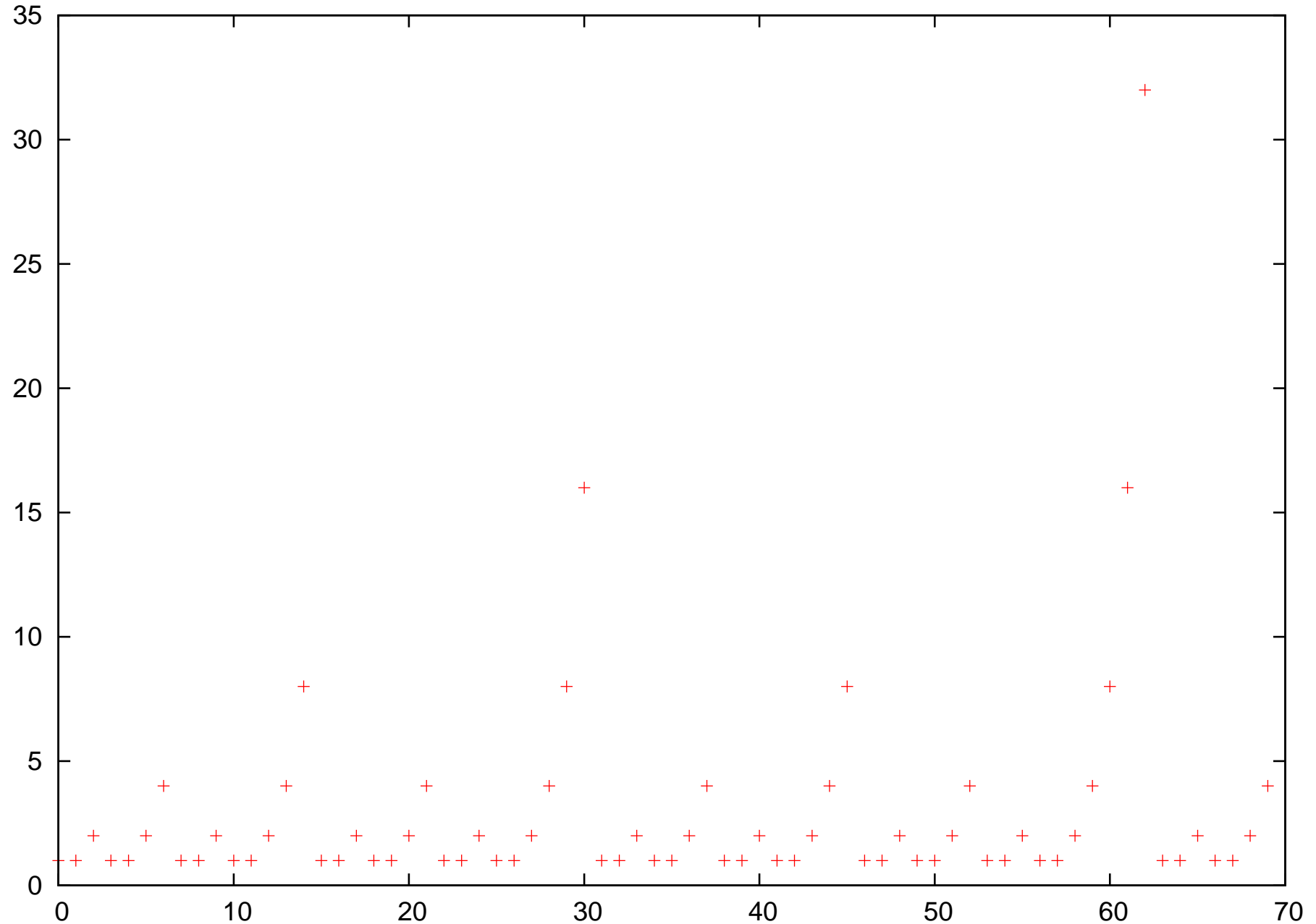
```
int inner = 100, outer = 100;
int restarts = 0, conflicts = 0;

for (;;)
{
    ... // run SAT core loop for 'inner' conflicts

    restarts++;
    conflicts += inner;

    if (inner >= outer)
    {
        outer *= 1.1;
        inner = 100;
    }
    else
        inner *= 1.1;
}
```

70 restarts in 104448 conflicts



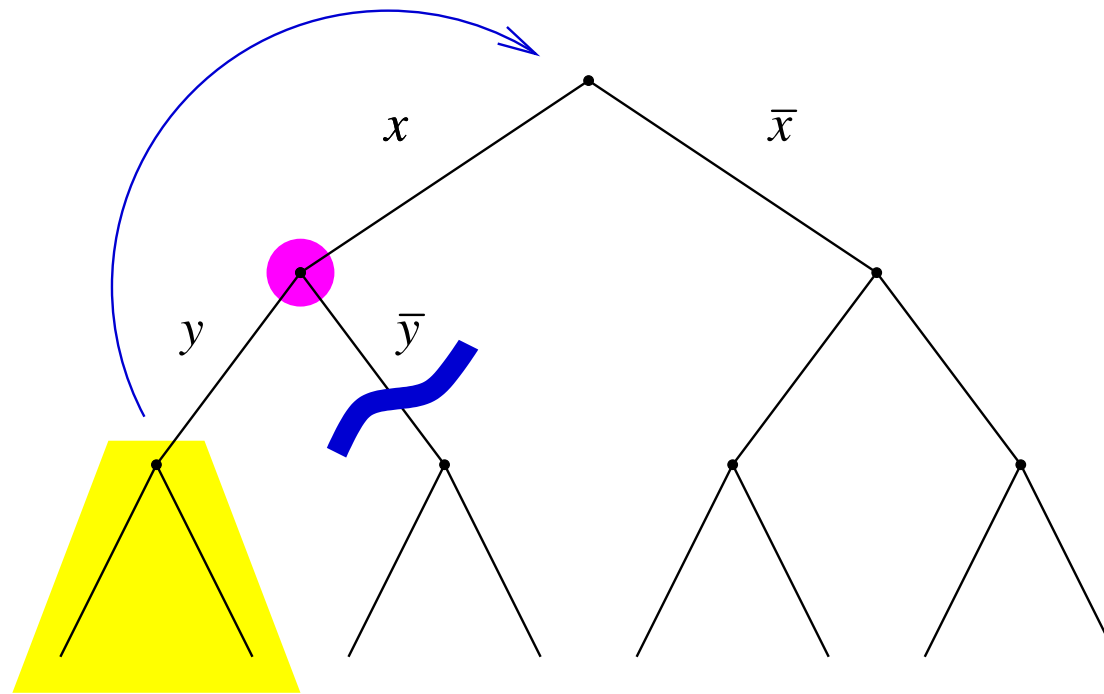
```
unsigned
luby (unsigned i)
{
    unsigned k;

    for (k = 1; k < 32; k++)
        if (i == (1 << k) - 1)
            return 1 << (k - 1);

    for (k = 1;; k++)
        if ((1 << (k - 1)) <= i && i < (1 << k) - 1)
            return luby (i - (1 << (k-1)) + 1);
}

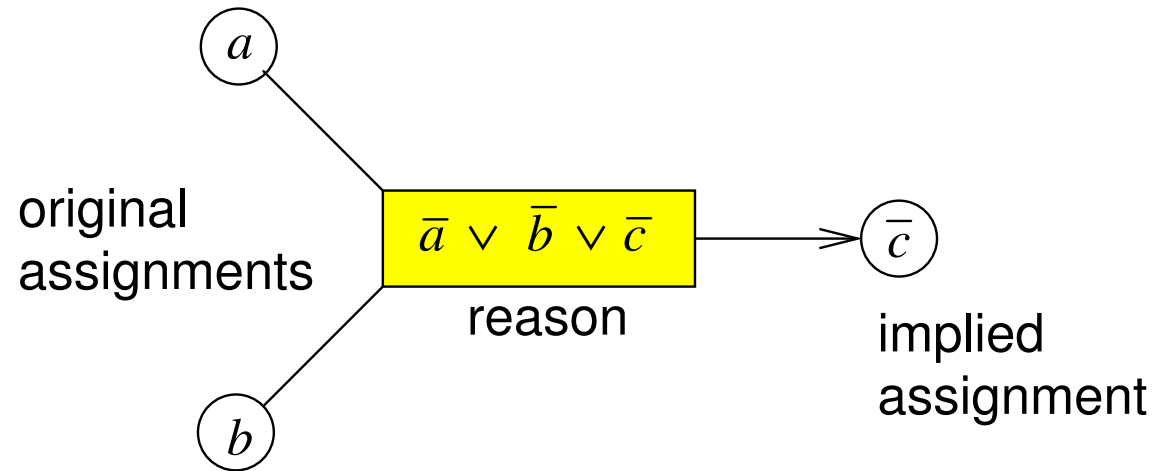
limit = 512 * luby (++restarts);
... // run SAT core loop for 'limit' conflicts
```

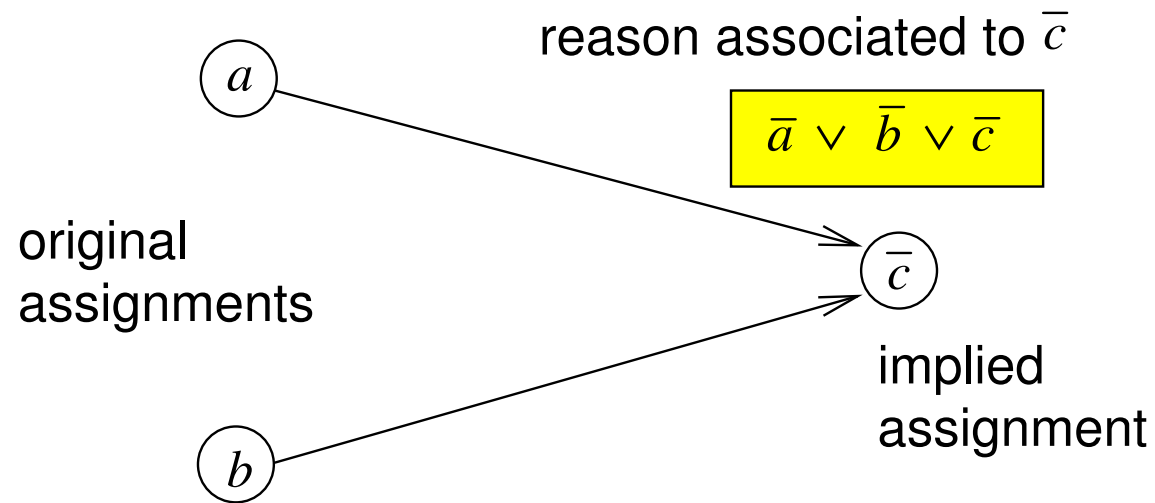
- phase assignment:
 - assign decision variable to 0 or 1?
 - only thing that matters in *satisfiable* instances
- “phase saving” as in RSat:
 - pick phase of last assignment (if not forced to, do not toggle assignment)
 - initially use statically computed phase (typically LIS)
 - so can be seen to maintain a *global full assignment*
- rapid restarts: varying restart interval with bursts of restarts
 - not only theoretically avoids local minima
 - works nicely together with phase saving



If y has never been used to derive a conflict, then skip \bar{y} case.

Immediately *jump back* to the \bar{x} case – assuming x was used.





-3 1 2 0

3 -1 0

3 -2 0

-4 -1 0

-4 -2 0

-3 4 0

3 -4 0

-3 5 6 0

3 -5 0

3 -6 0

4 5 6 0

We use a version of the DIMACS format.

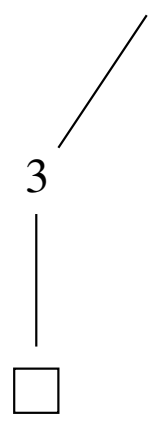
Variables are represented as positive integers.

Integers represent literals.

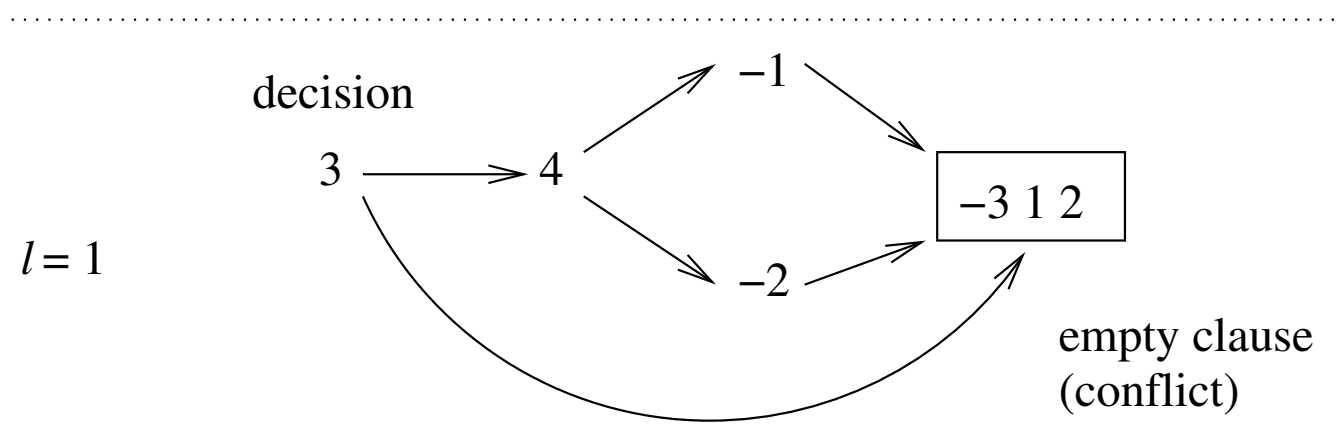
Subtraction means negation.

A clause is a zero terminated list of integers.

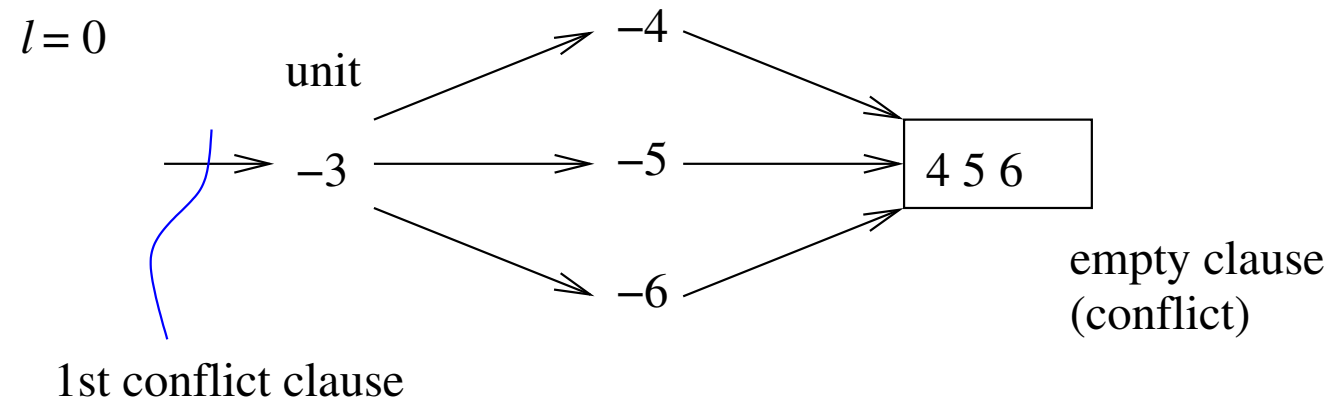
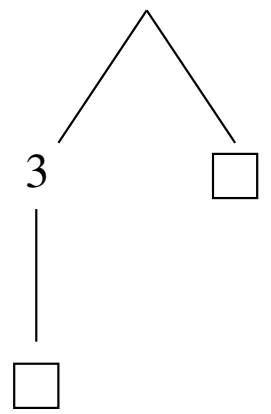
CNF has a good cut made of variables 3 and 4
(but we are going to apply DP with learning to it)



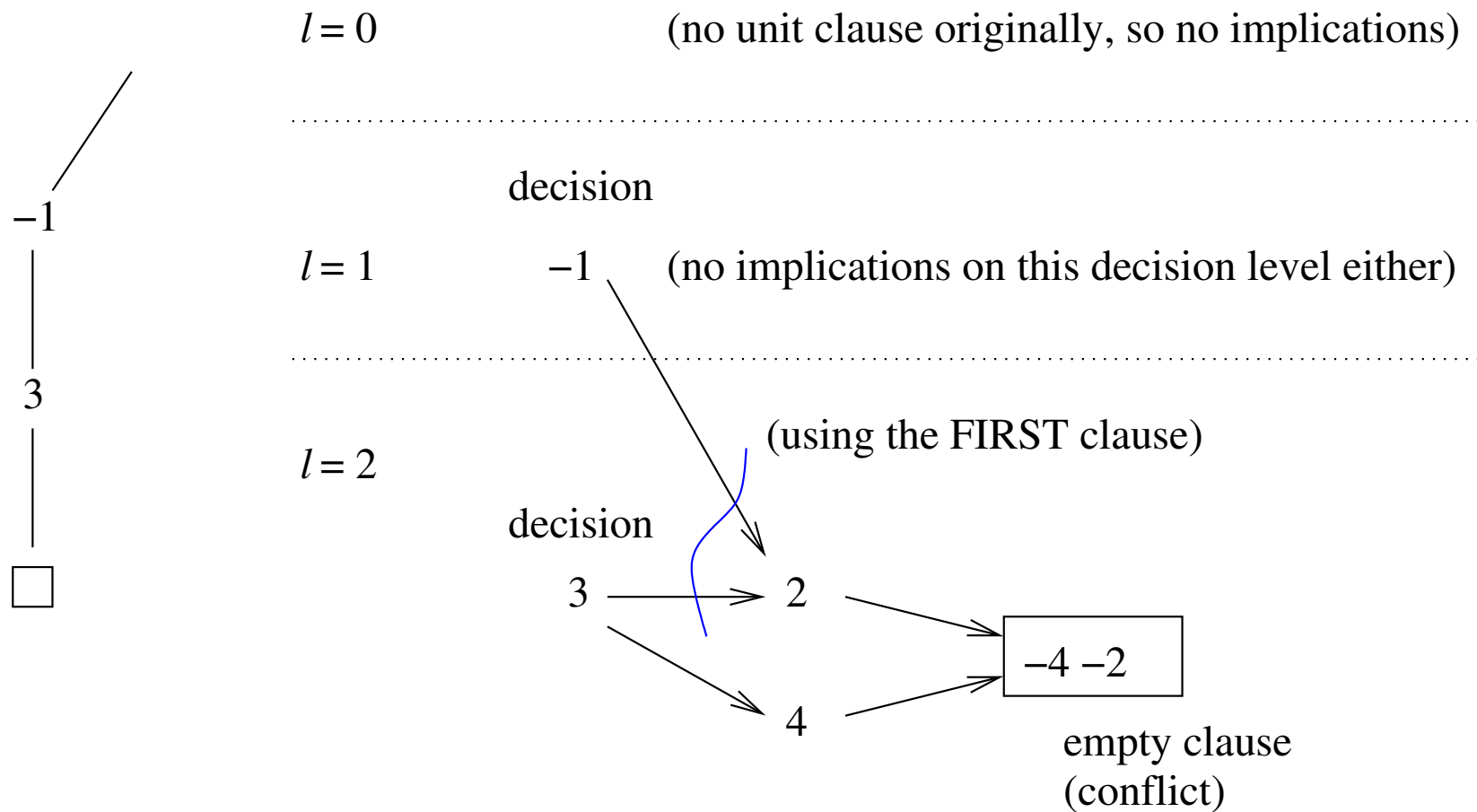
$l=0$ (no unit clause originally, so no implications)



unit clause -3 is generated as learned clause and we backtrack to $l=0$

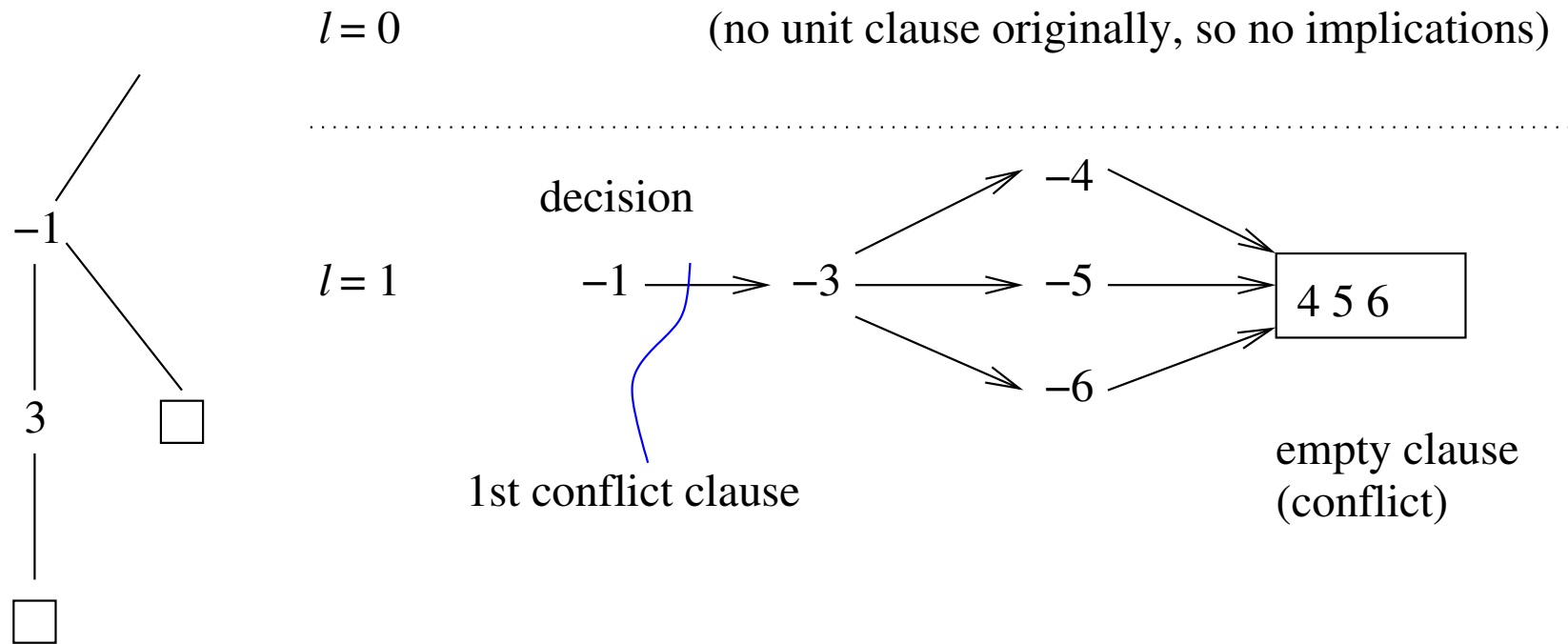


since -3 has a real unit clause as reason, an empty conflict clause is learned



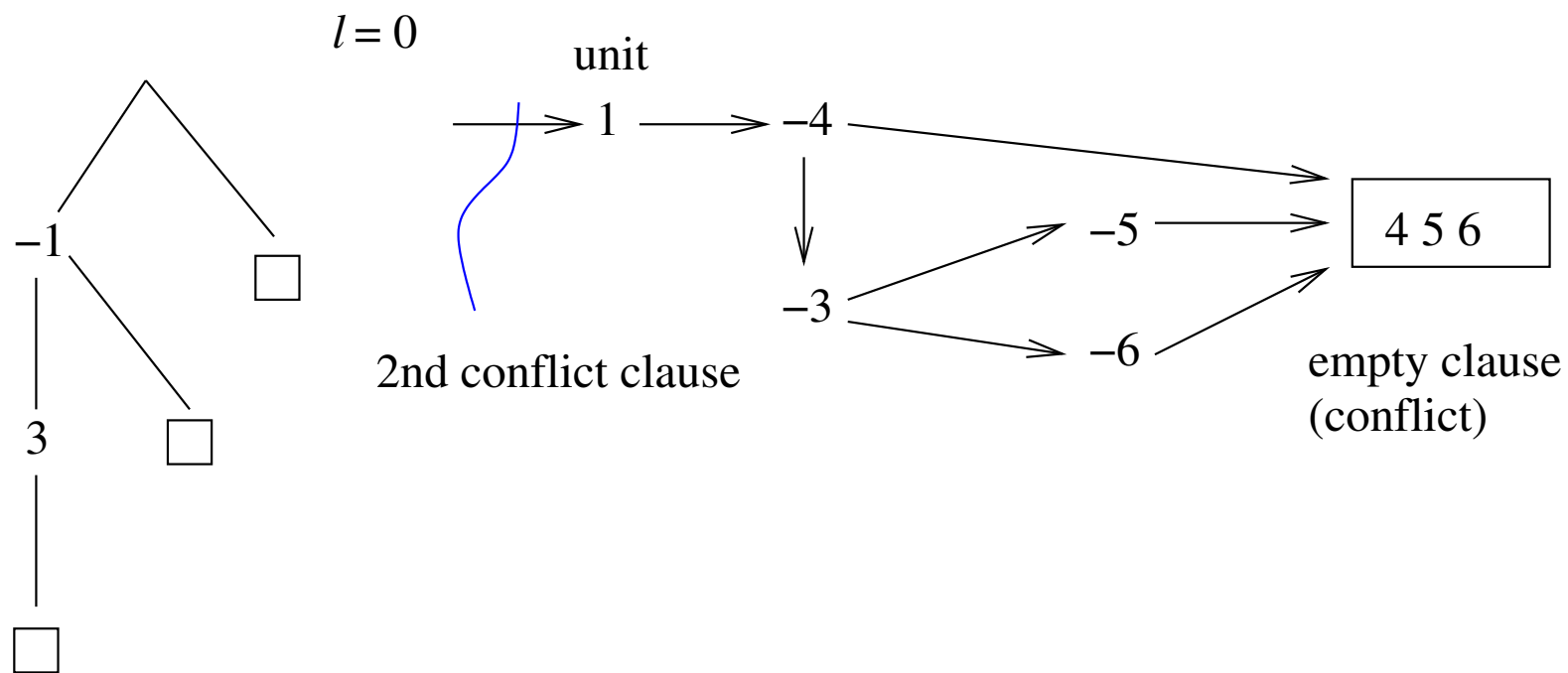
since FIRST clause was used to derive 2, conflict clause is (1 -3)

backtrack to $l = 1$ (smallest level for which conflict clause is a unit clause)

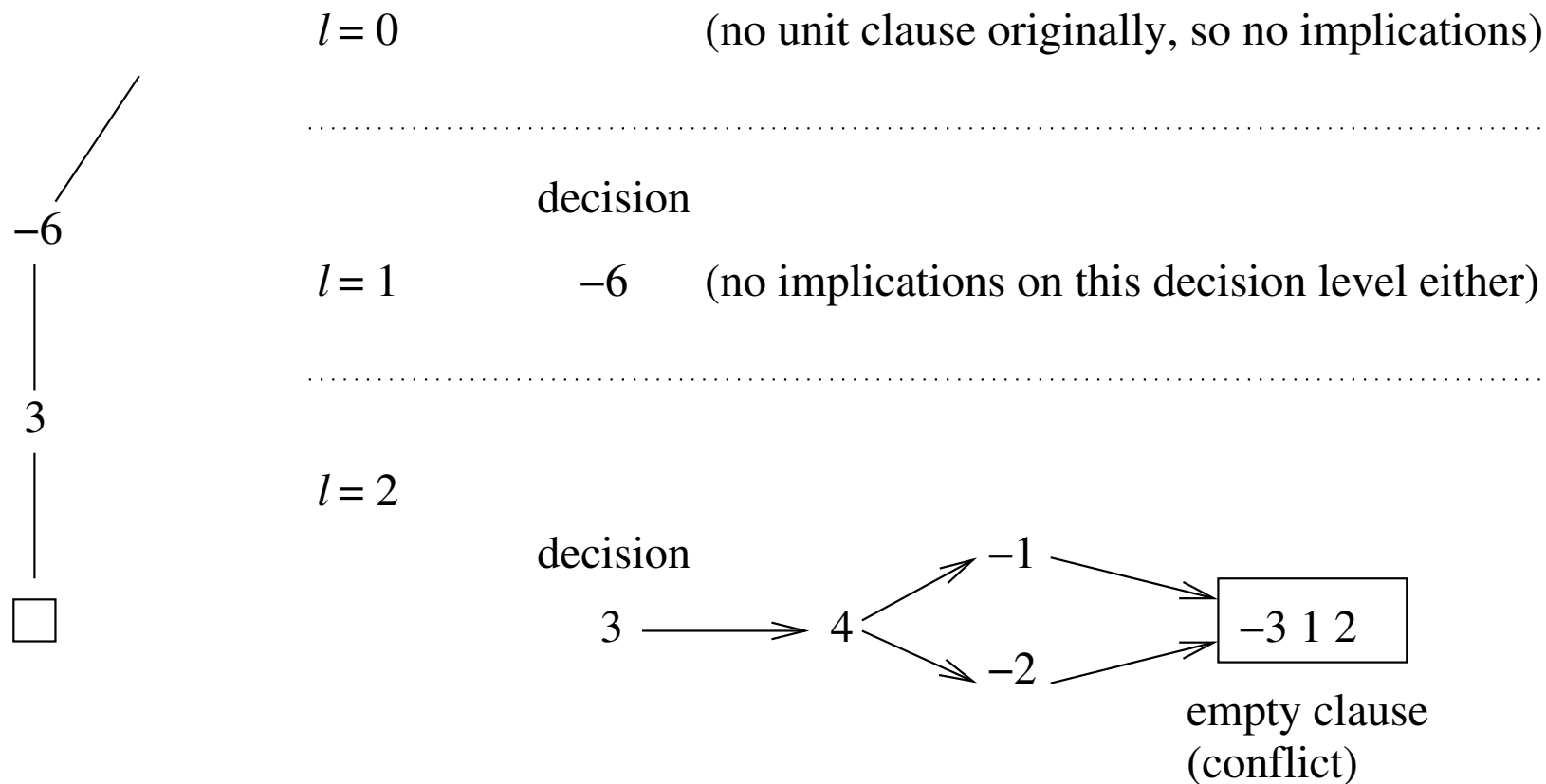


learned conflict clause is the unit clause 1

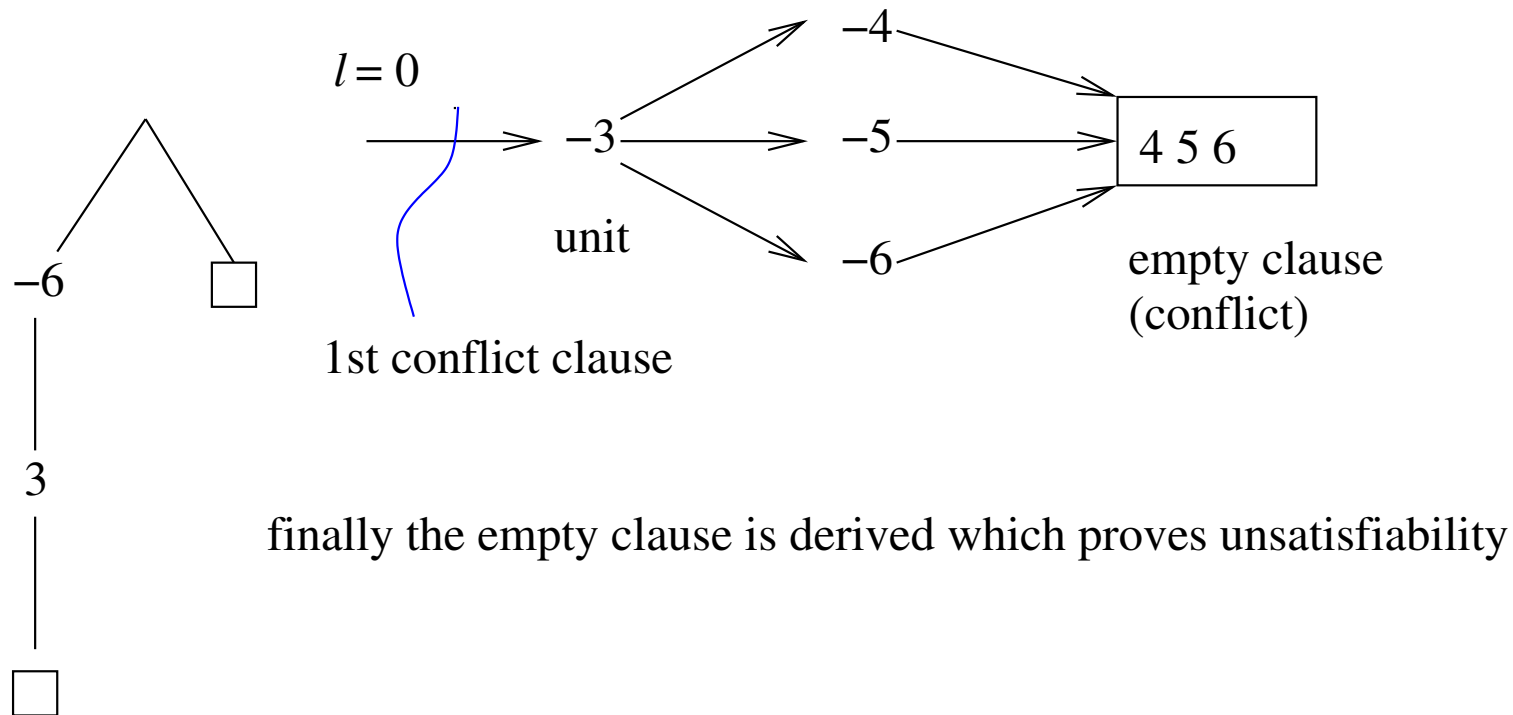
backtrack to decision level $l = 0$



since the learned clause is the empty clause, conclude unsatisfiability



learn the unit clause -3 and BACKJUMP to decision level $l = 0$



```
int
sat (Solver solver)
{
    Clause conflict;

    for (;;)
    {
        if (bcp_queue_is_empty (solver) && time_to_simplify (solver))
            simplify (solver);
        if (bcp_queue_is_empty (solver)) {
            if (all_variables_assigned (solver)) return SATISFIABLE;
            if (should_restart (solver)) restart ();
            decide (solver);
        }
        conflict = bcp (solver);
        if (conflict && !backtrack (solver, conflict)) return UNSATISFIABLE;
    }
}
```

```
int
backtrack (Solver solver, Clause conflict)
{
    Clause learned_clause; Assignment assignment; int new_level;

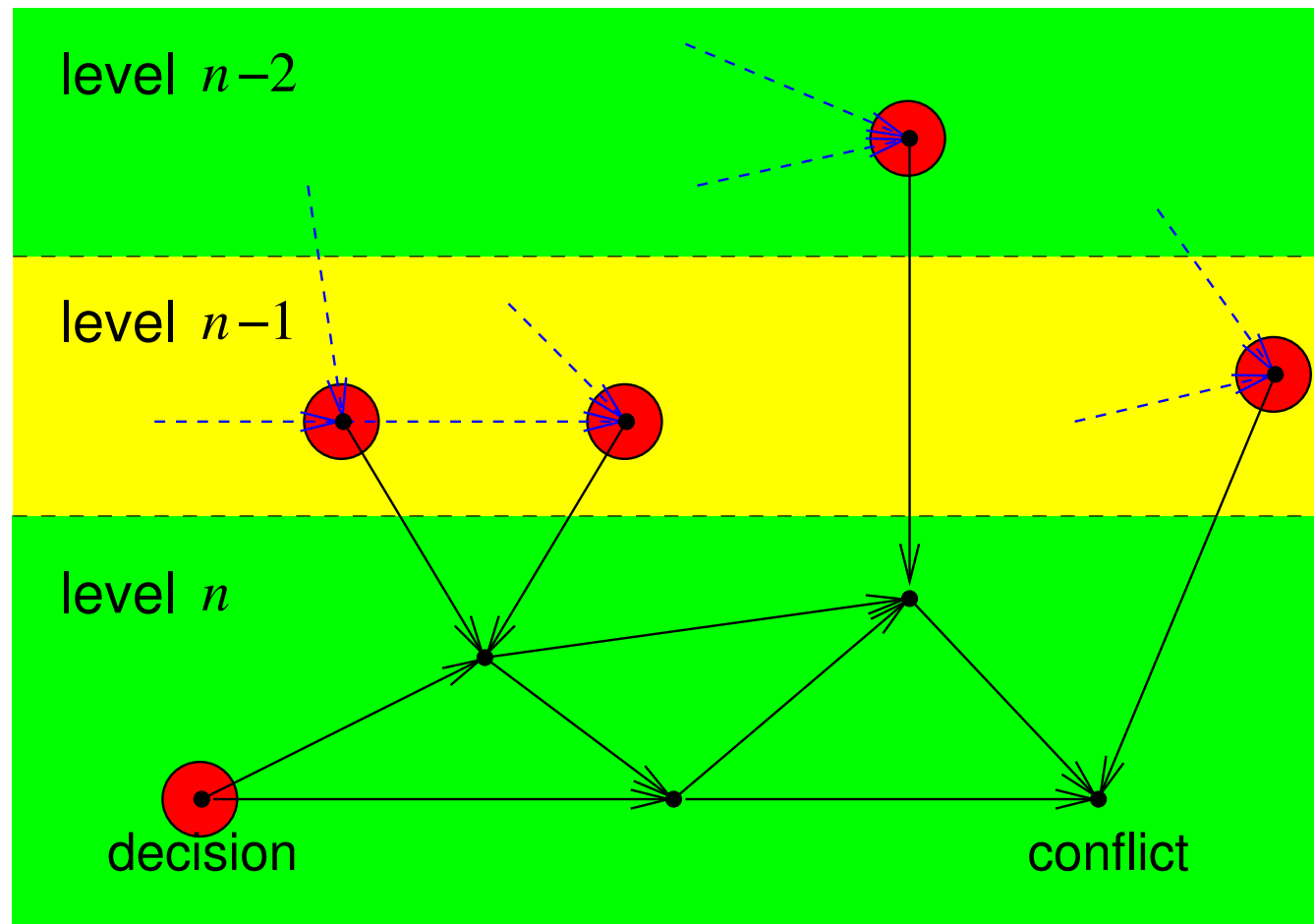
    if (decision_level(solver) == 0)
        return 0;

    analyze (solver, conflict);
    learned_clause = add (solver);

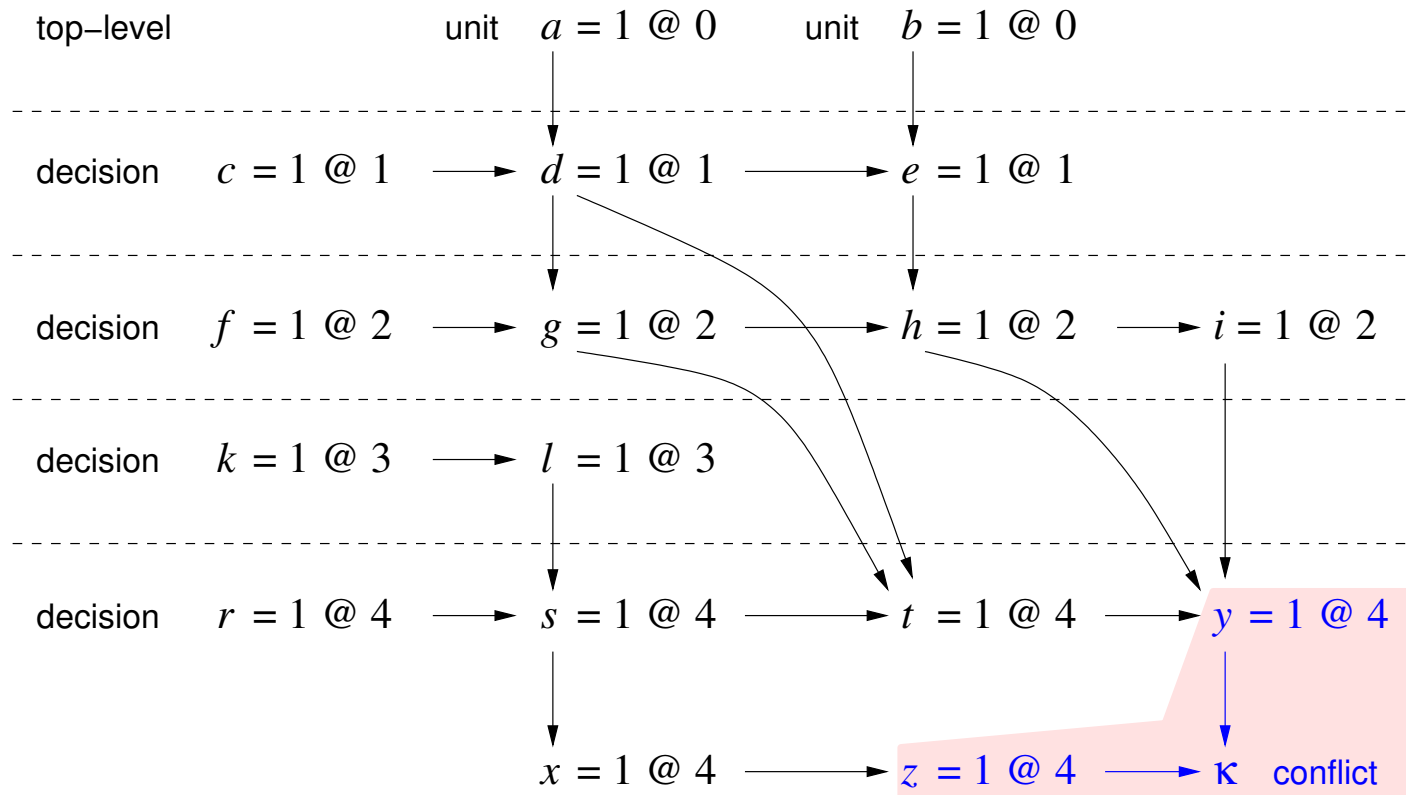
    assignment = drive (solver, learned_clause);
    enqueue_bcp_queue (solver, assignment);

    new_level = jump (solver, learned_clause);
    undo (solver, new_level);

    return 1;
}
```

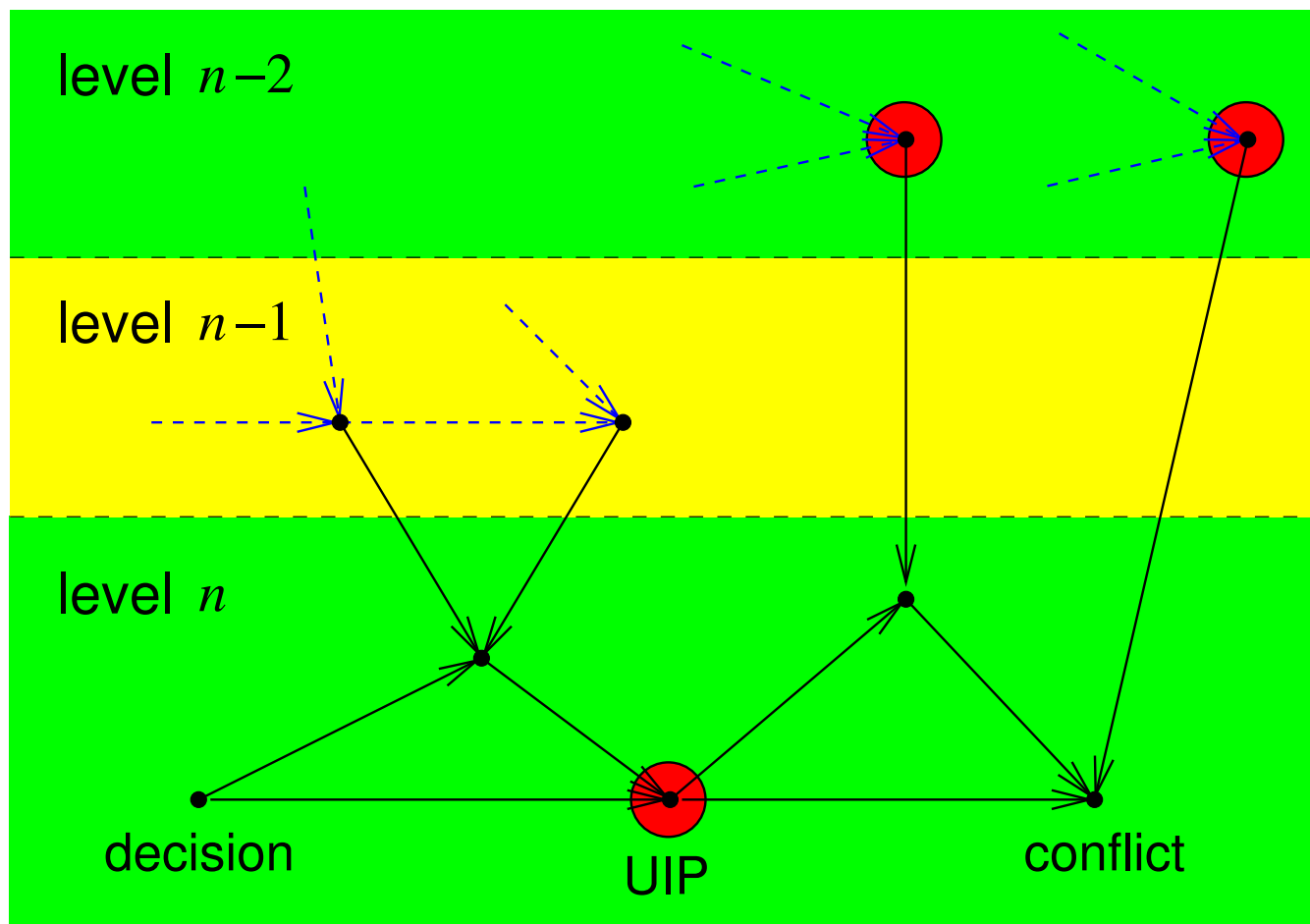


a simple cut always exists: set of roots (decisions) contributing to the conflict



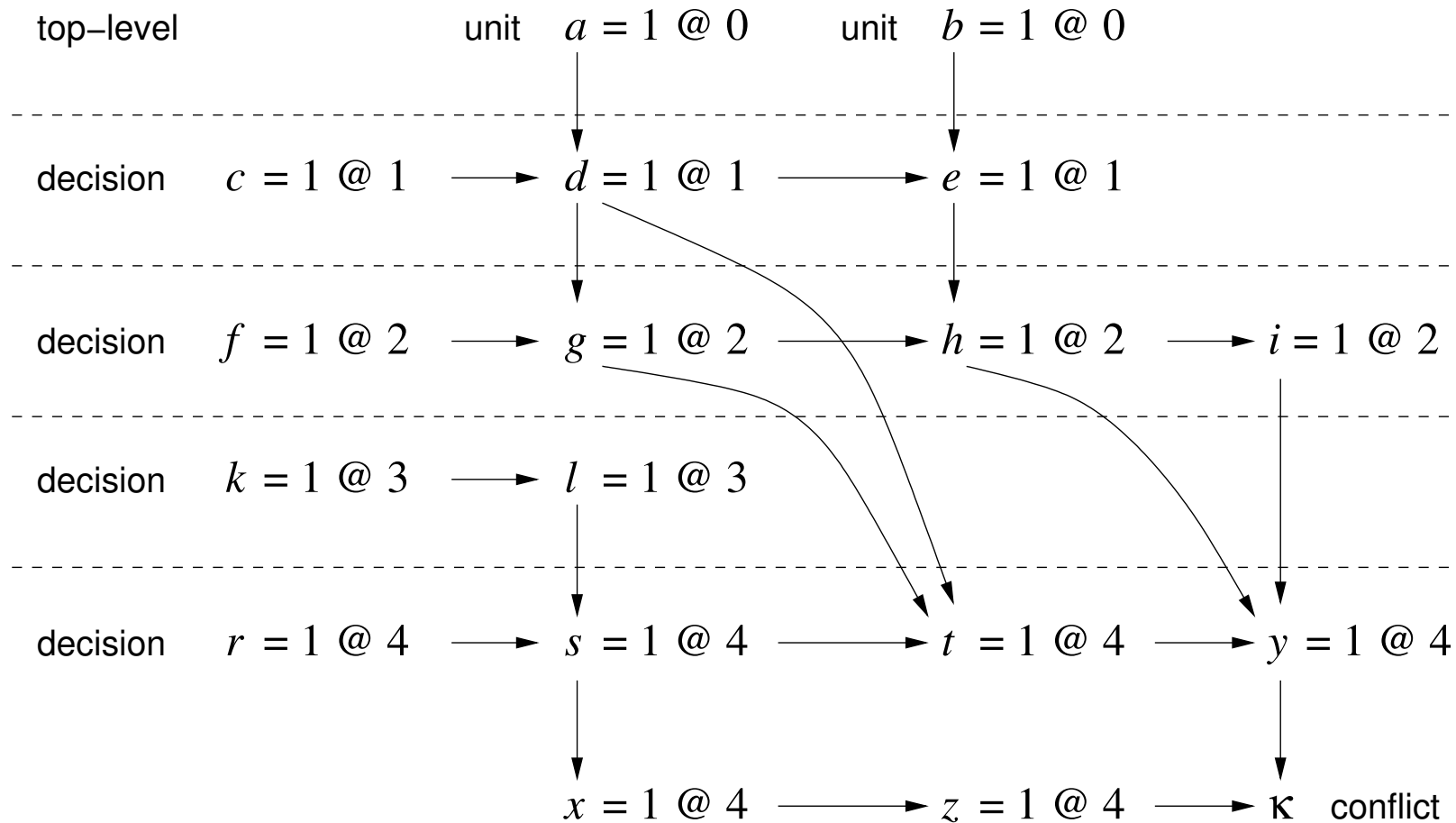
UIP = *articulation point* in graph decomposition into biconnected components (simply a node which, if removed, would disconnect two parts of the graph)

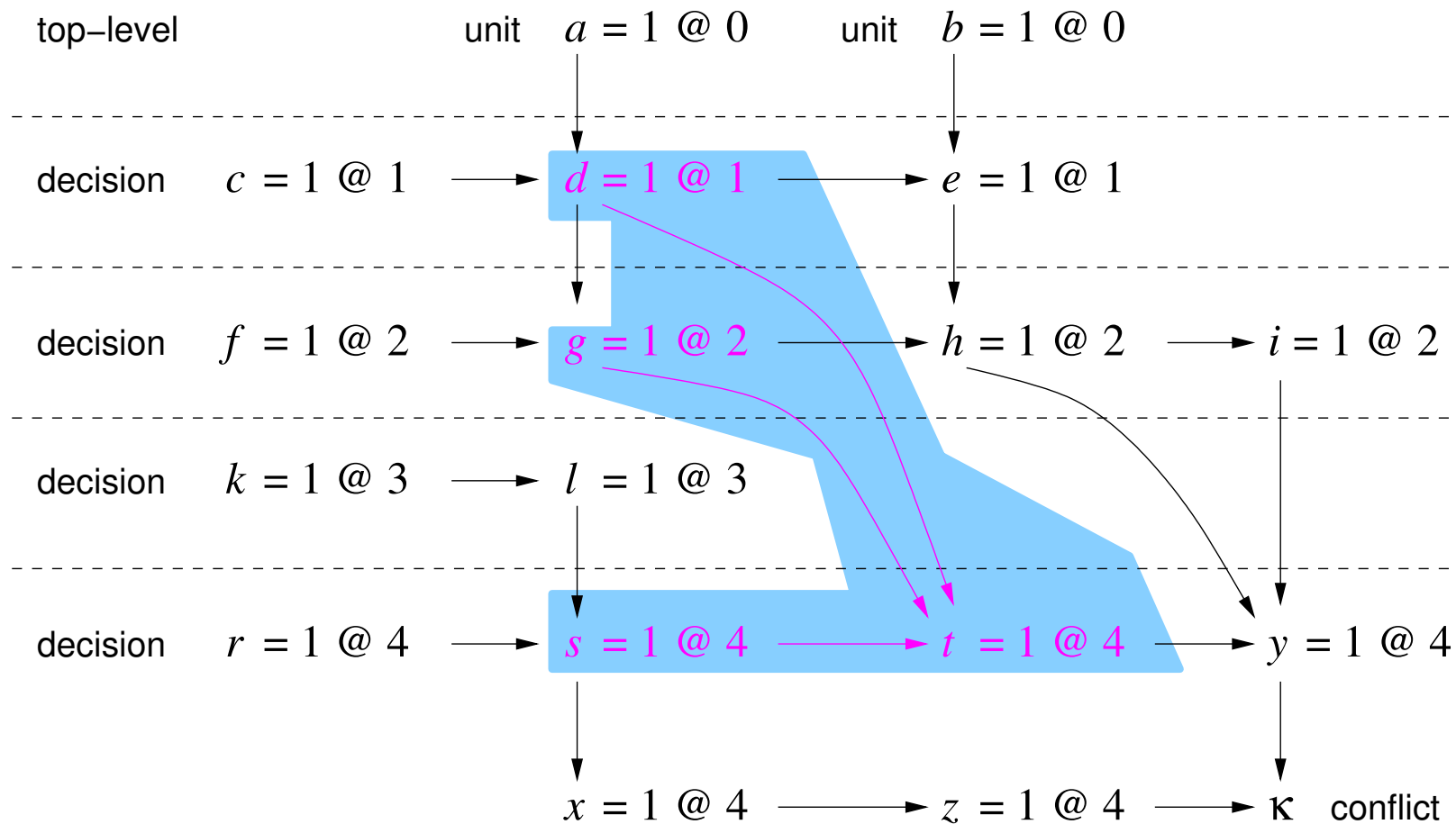
- can be found by graph traversal in the order of made assignments
- *trail* respects this order
- traverse reasons of variables on trail starting with conflict
- count “open paths”
(initially size of clause with only false literals)
- if all paths converged at one node, then UIP is found
- decision of current decision level is a UIP and thus a *sentinel*



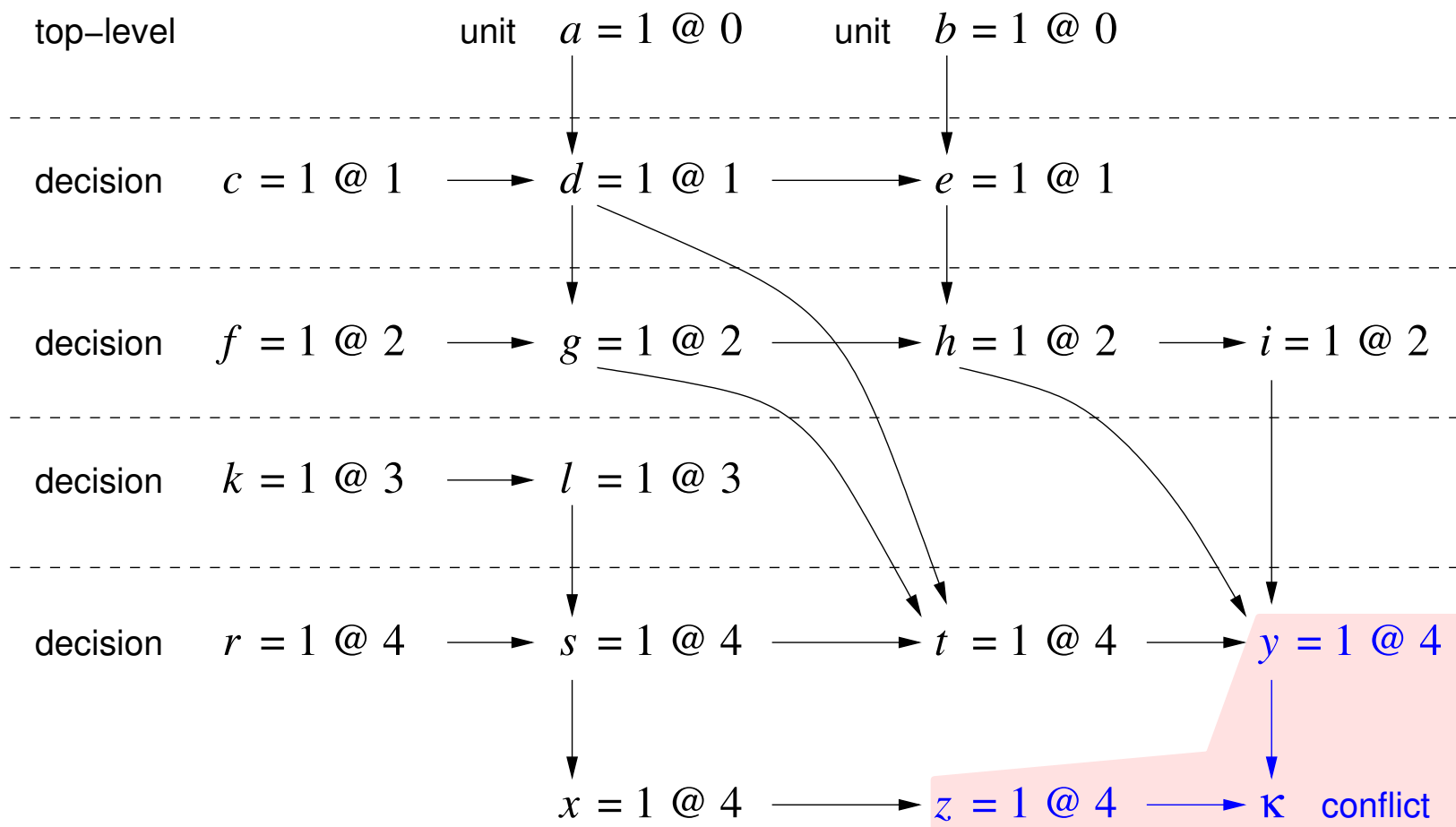
1st UIP learned clause increases chance of backjumping
 (“pulls in” as few decision levels as possible)

- intuitively it is important to localize the search (cf cutwidth heuristics)
- cuts for learned clauses may only include UIPs of current decision level
- on lower decision levels an arbitrary cut can be chosen
- multiple alternatives
 - include all the roots contributing to the conflict
 - find minimal cut (heuristically)
 - **cut off at first literal of lower decision level** (works best)

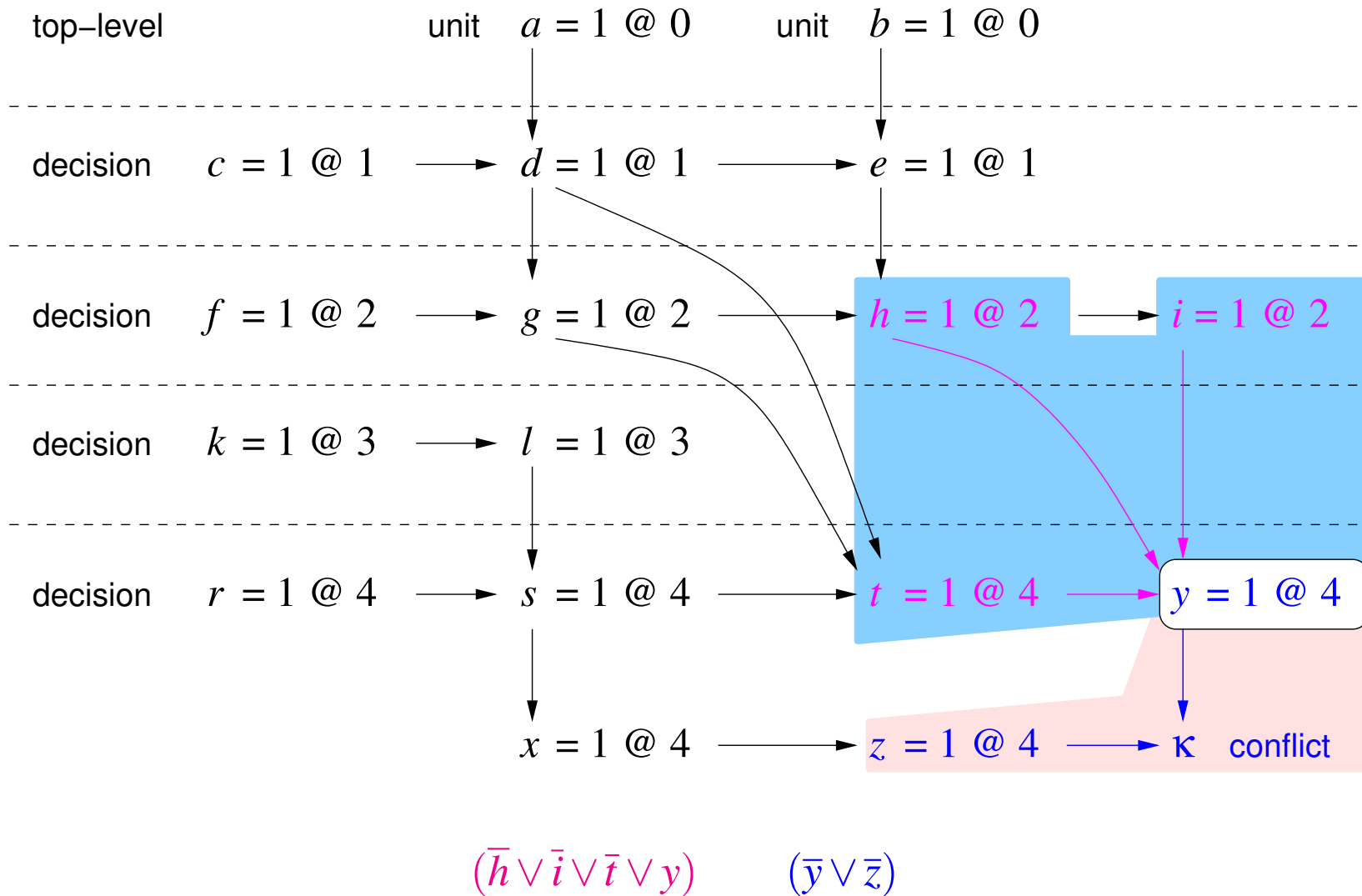


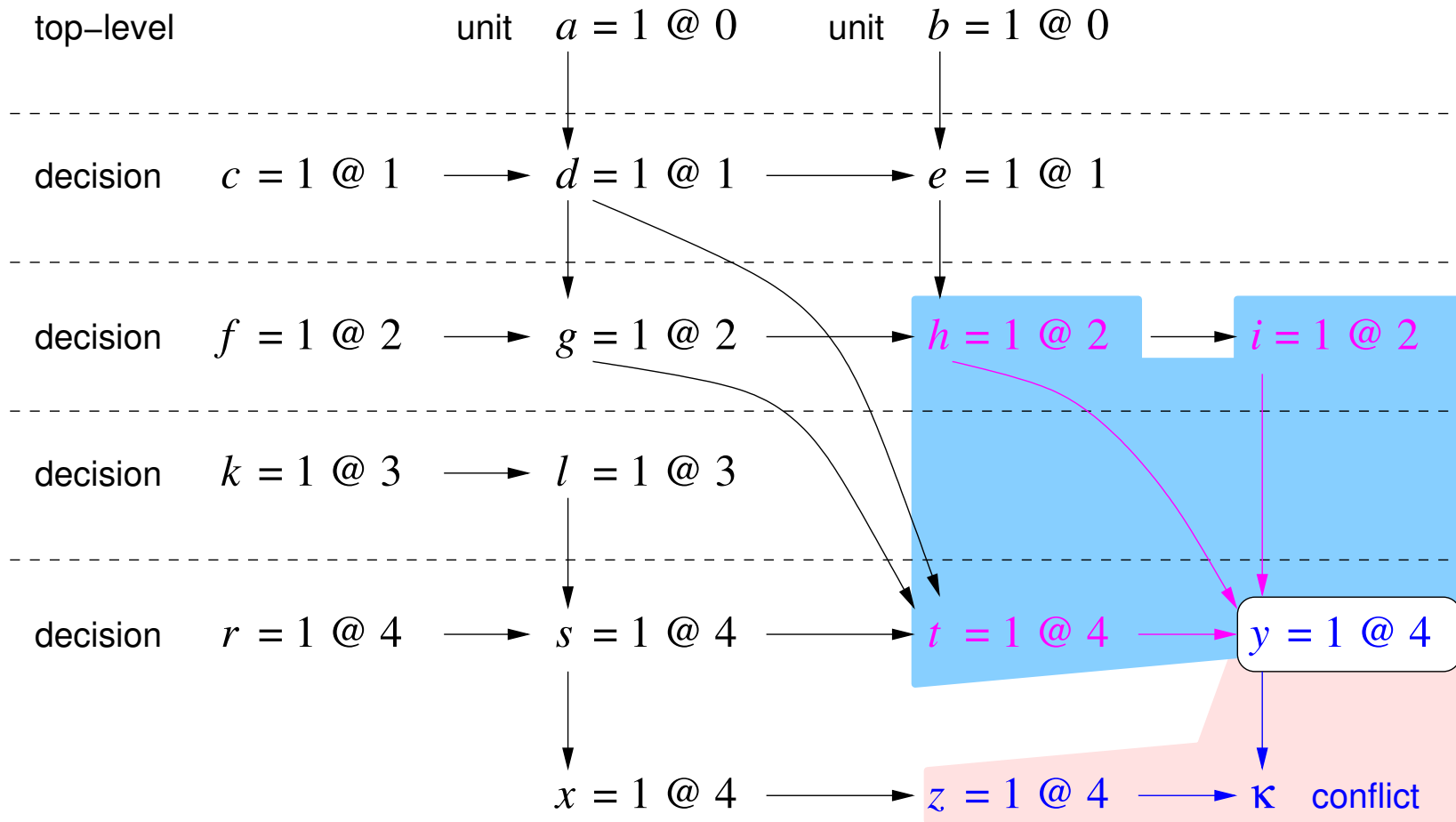


$$d \wedge g \wedge s \rightarrow t \quad \equiv \quad (\bar{d} \vee \bar{g} \vee \bar{s} \vee t)$$

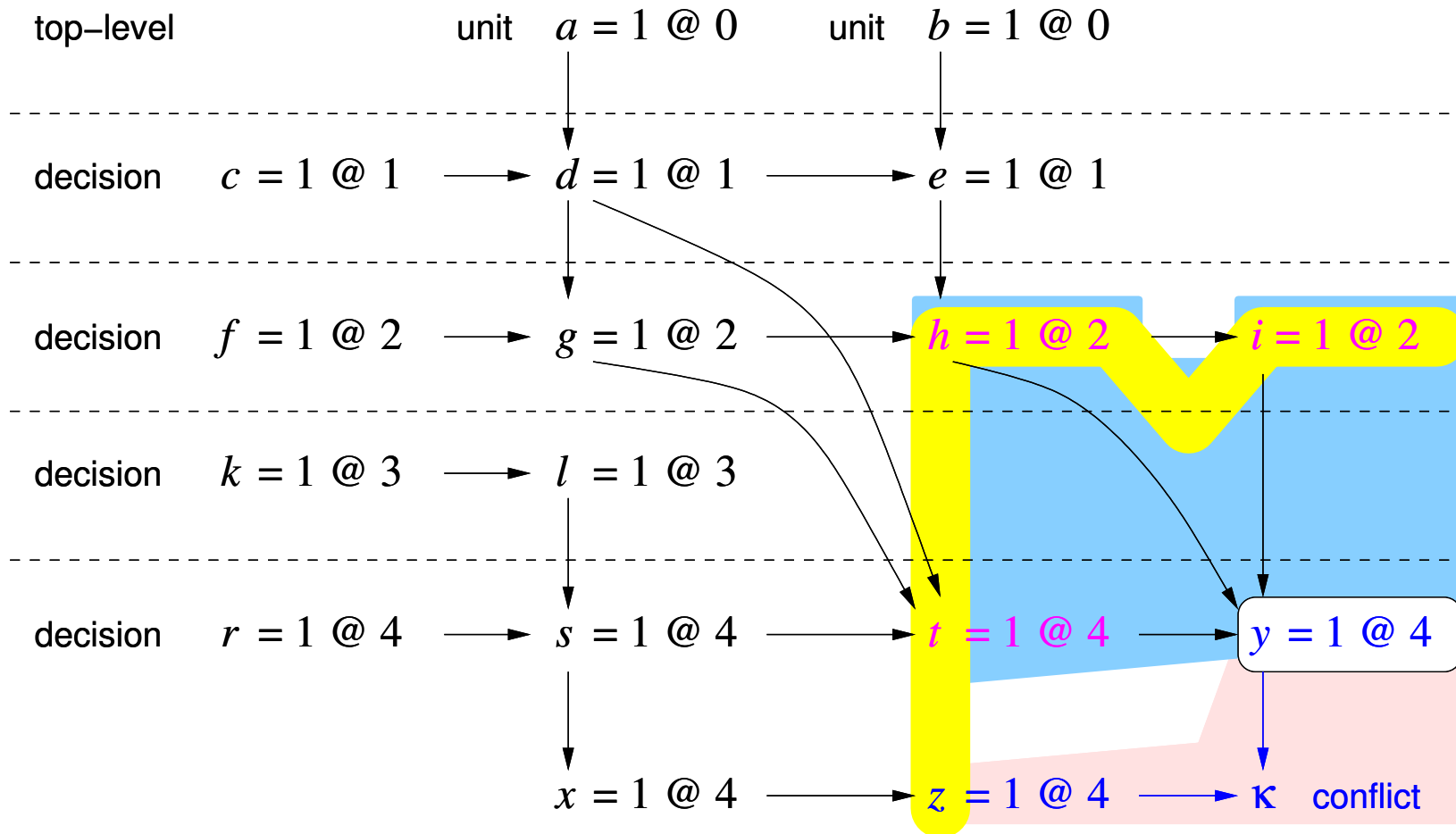


$$\neg(y \wedge z) \equiv (\bar{y} \vee \bar{z})$$

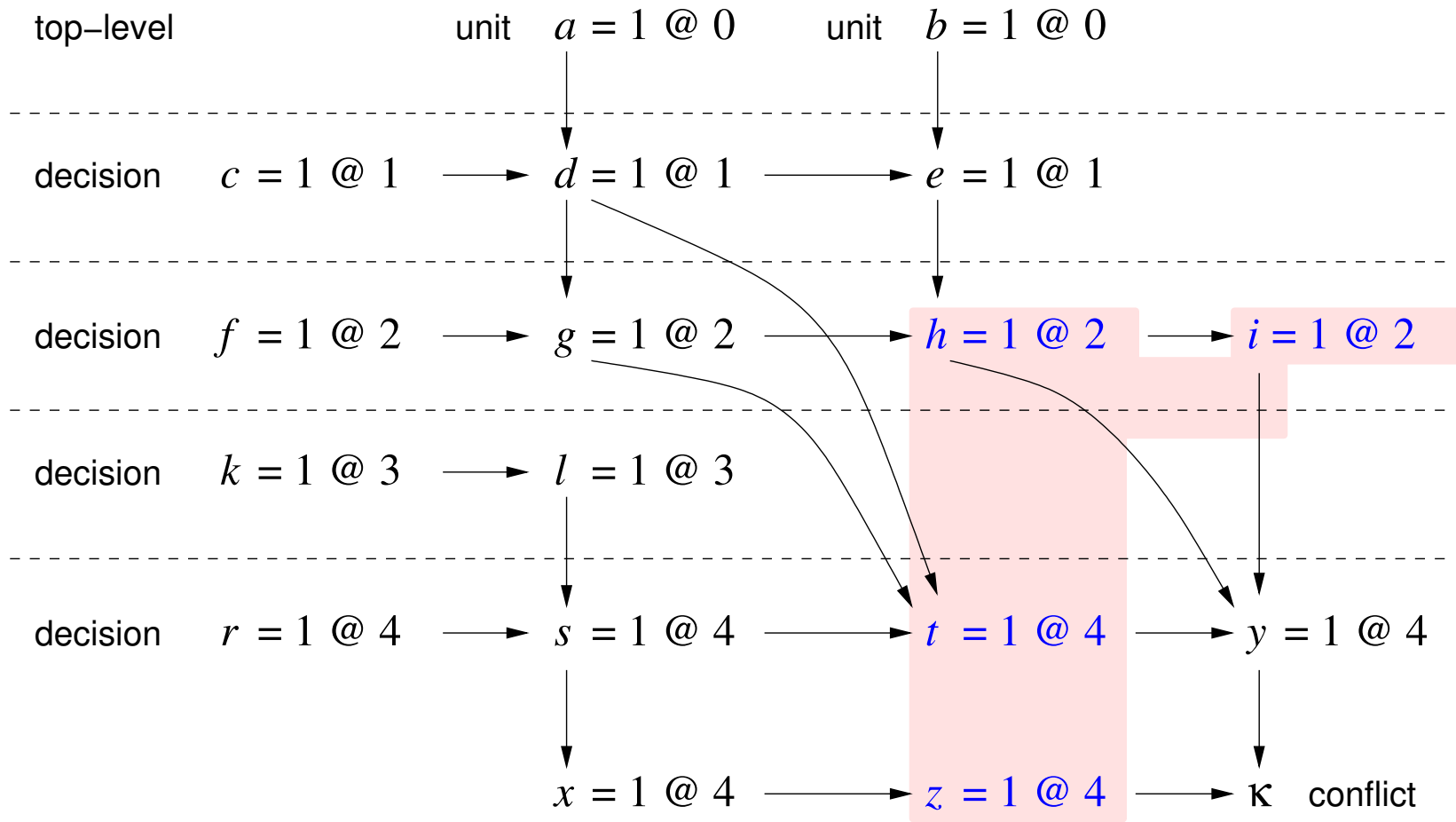




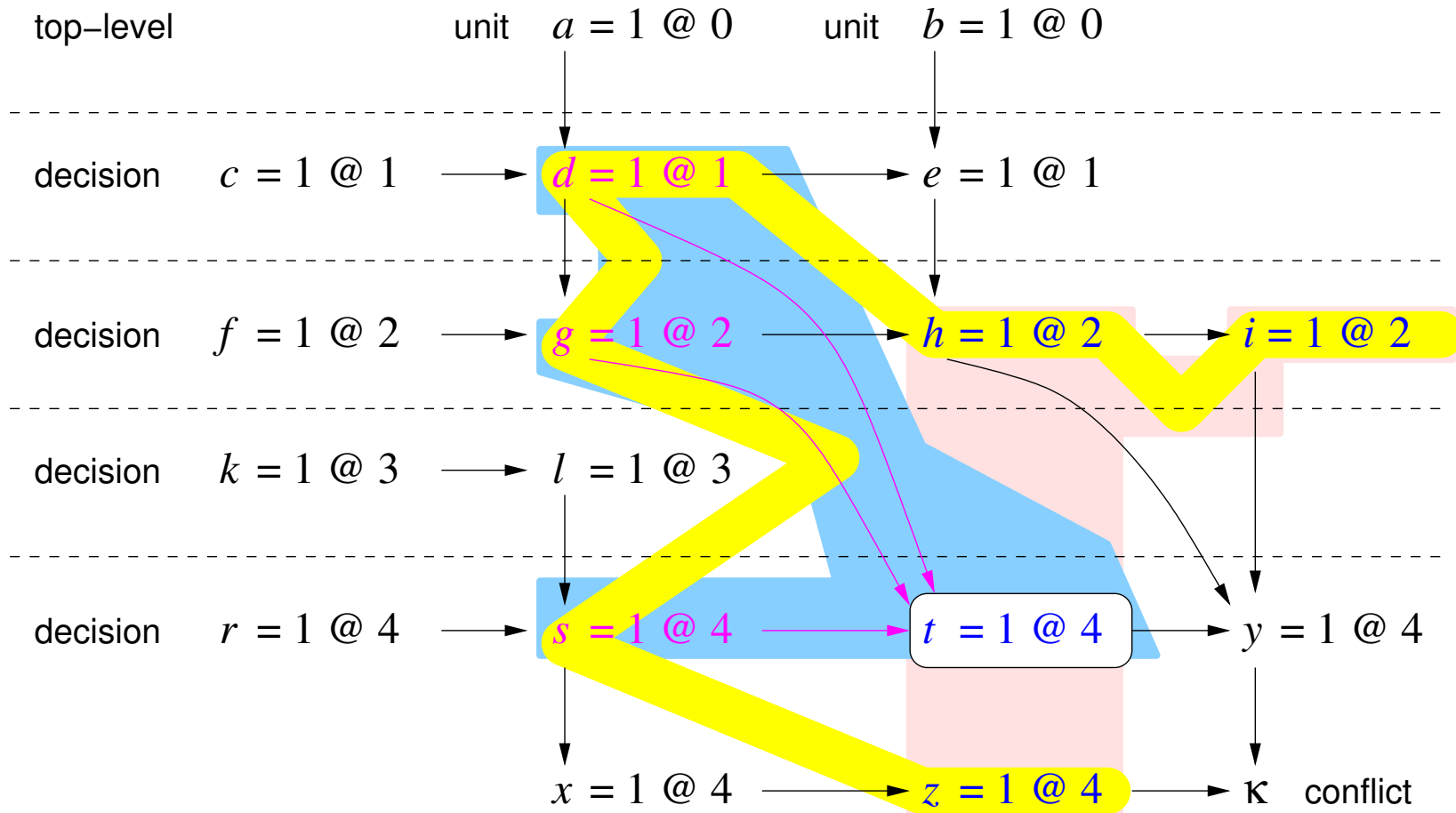
$$\frac{(\bar{h} \vee \bar{i} \vee \bar{t} \vee y) \quad (\bar{y} \vee \bar{z})}{(\bar{h} \vee \bar{i} \vee \bar{t} \vee \bar{z})}$$



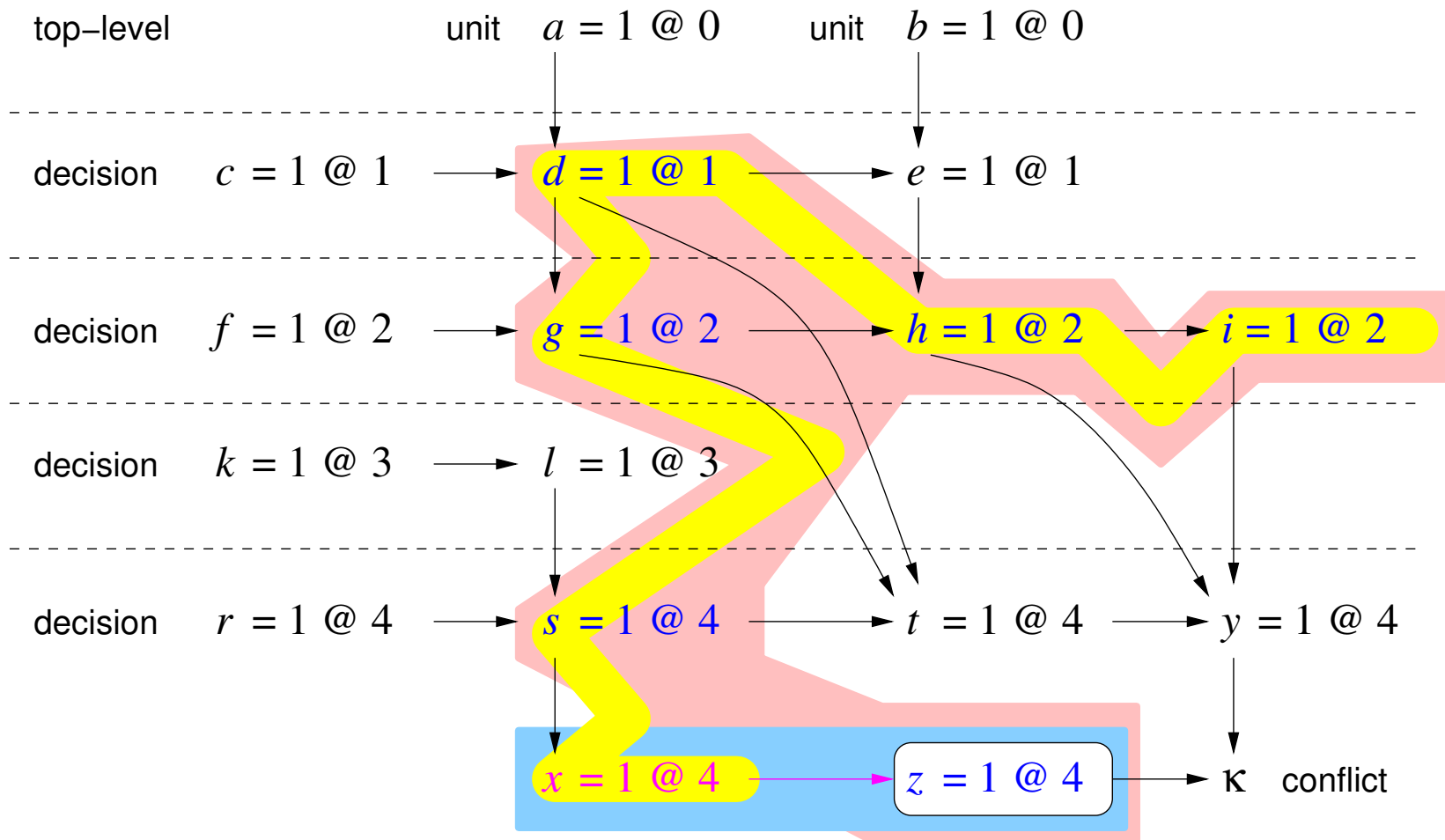
$$\frac{(\bar{h} \vee \bar{i} \vee \bar{t} \vee y) \quad (\bar{y} \vee \bar{z})}{(\bar{h} \vee \bar{i} \vee \bar{t} \vee \bar{z})}$$



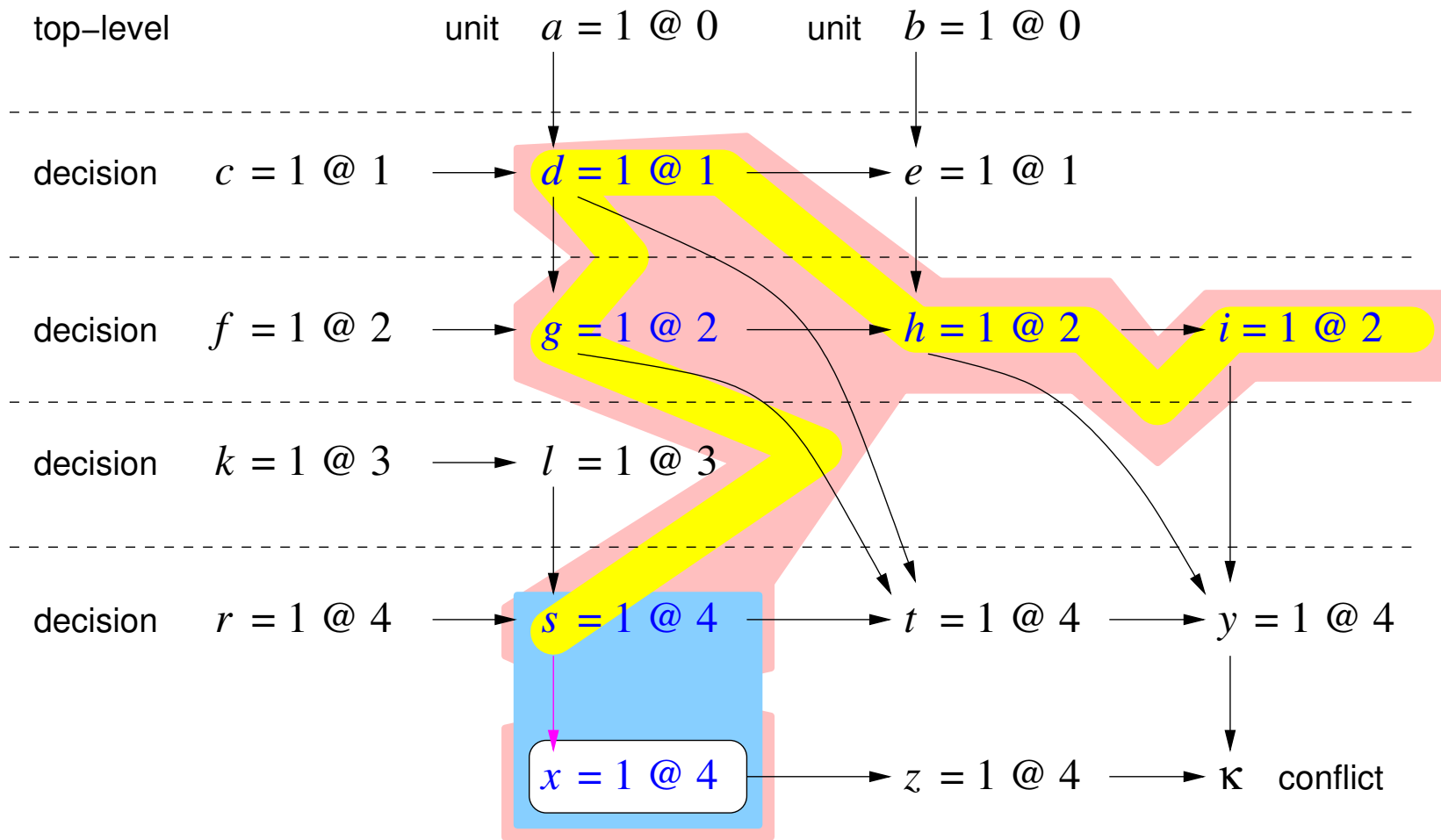
$$(\bar{h} \vee \bar{i} \vee \bar{t} \vee \bar{z})$$



$$\frac{(\bar{d} \vee \bar{g} \vee \bar{s} \vee t) \quad (\bar{h} \vee \bar{i} \vee \bar{t} \vee \bar{z})}{(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i} \vee \bar{z})}$$

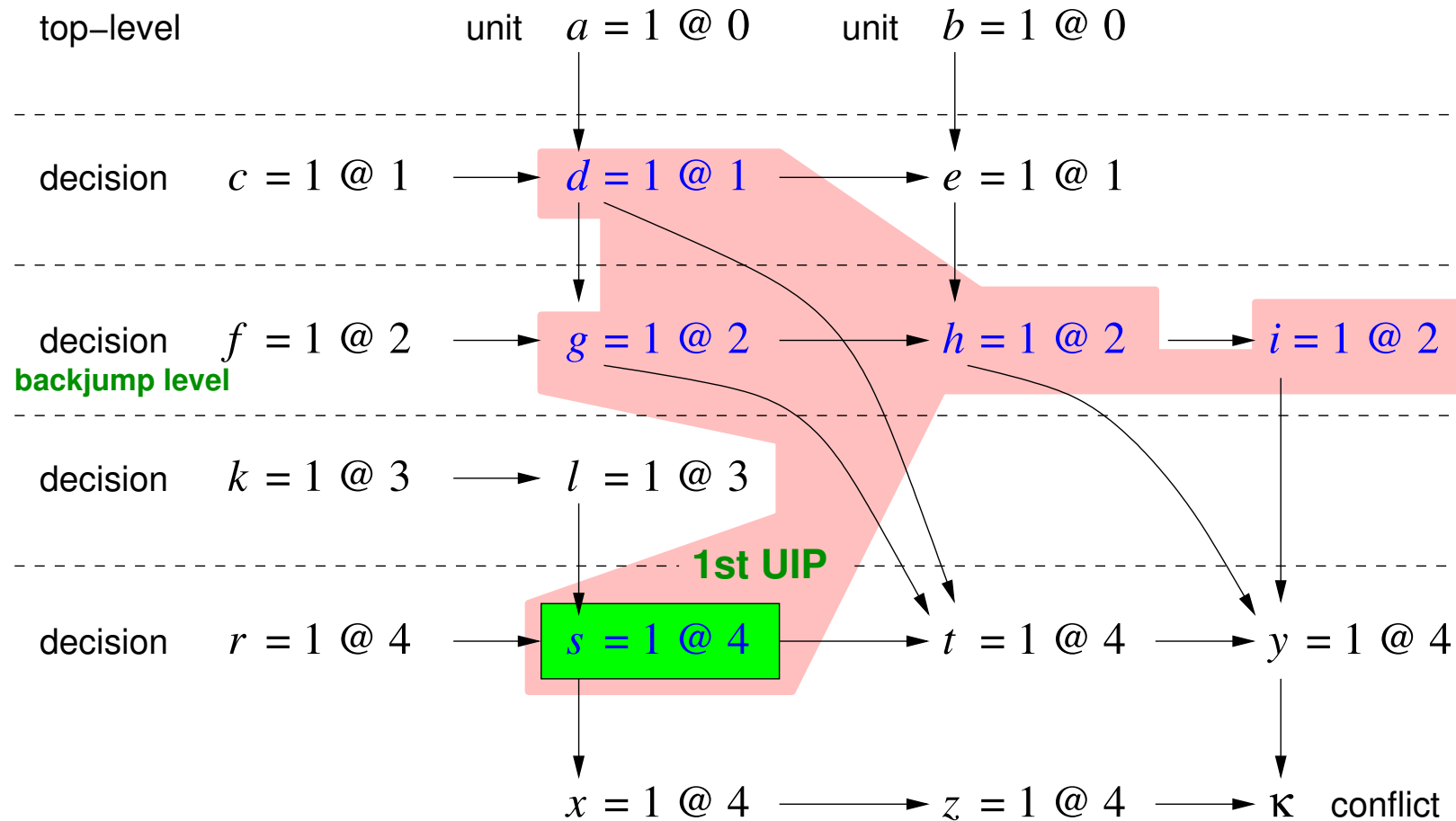


$$\frac{(\bar{x} \vee z) \quad (\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i} \vee \bar{z})}{(\bar{x} \vee \bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})}$$

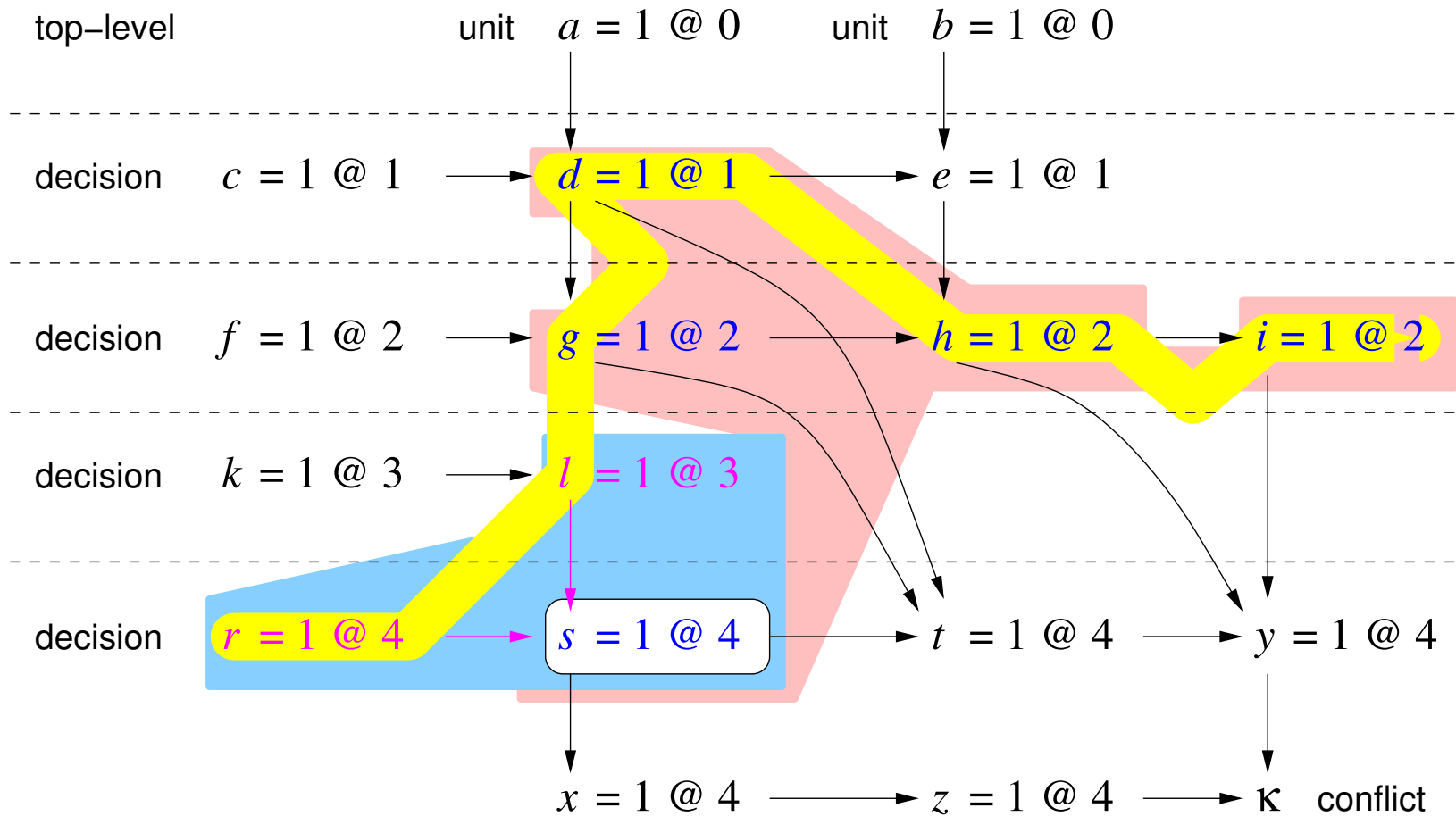


$$\frac{(\bar{s} \vee x) \quad (\bar{x} \vee \bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})}{(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})}$$

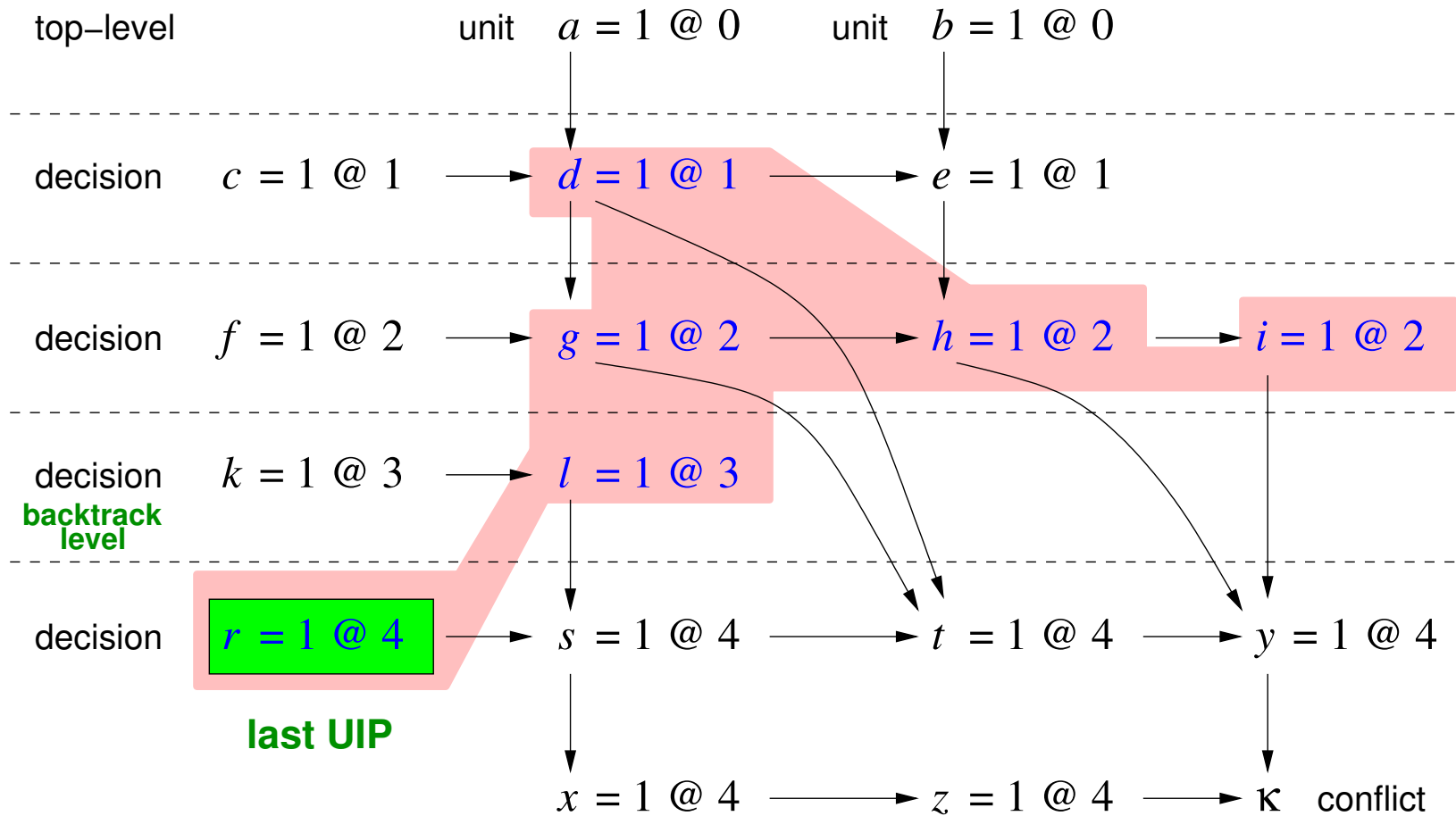
self subsuming resolution



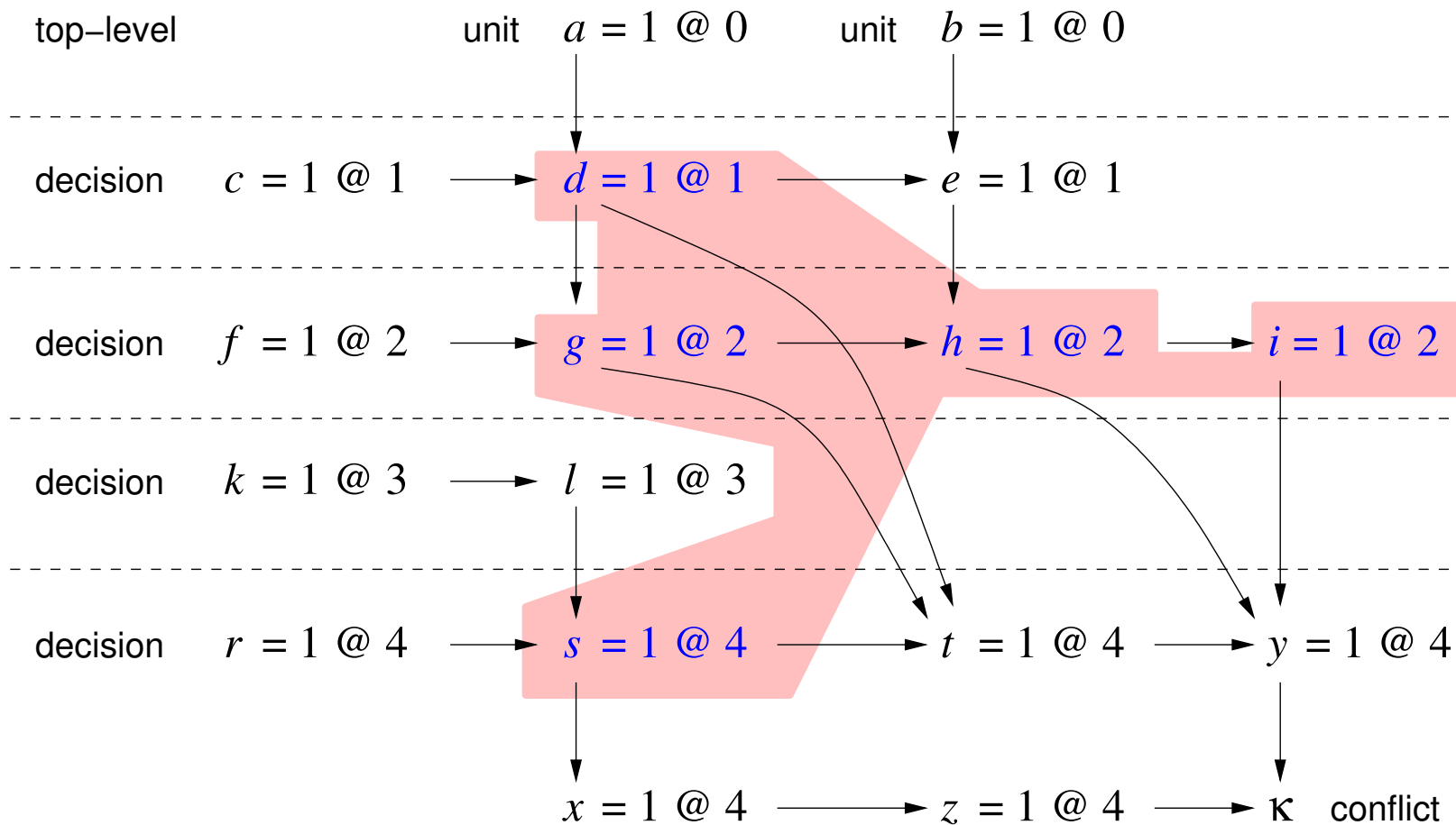
$$(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})$$



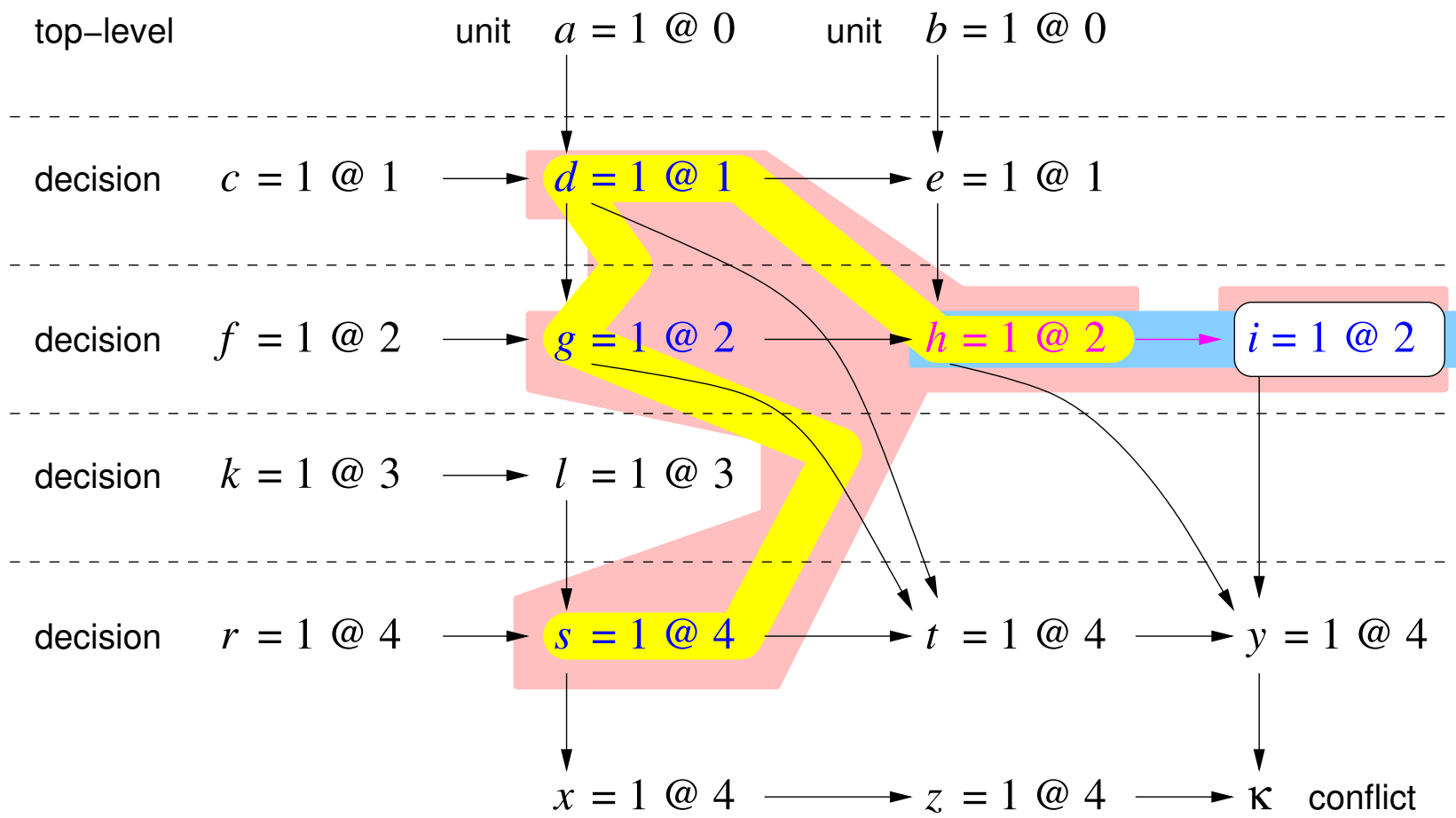
$$\frac{(\bar{l} \vee \bar{r} \vee s) \quad (\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})}{(\bar{l} \vee \bar{r} \vee \bar{d} \vee \bar{g} \vee \bar{h} \vee \bar{i})}$$



$$(\bar{d} \vee \bar{g} \vee \bar{l} \vee \bar{r} \vee \bar{h} \vee \bar{i})$$

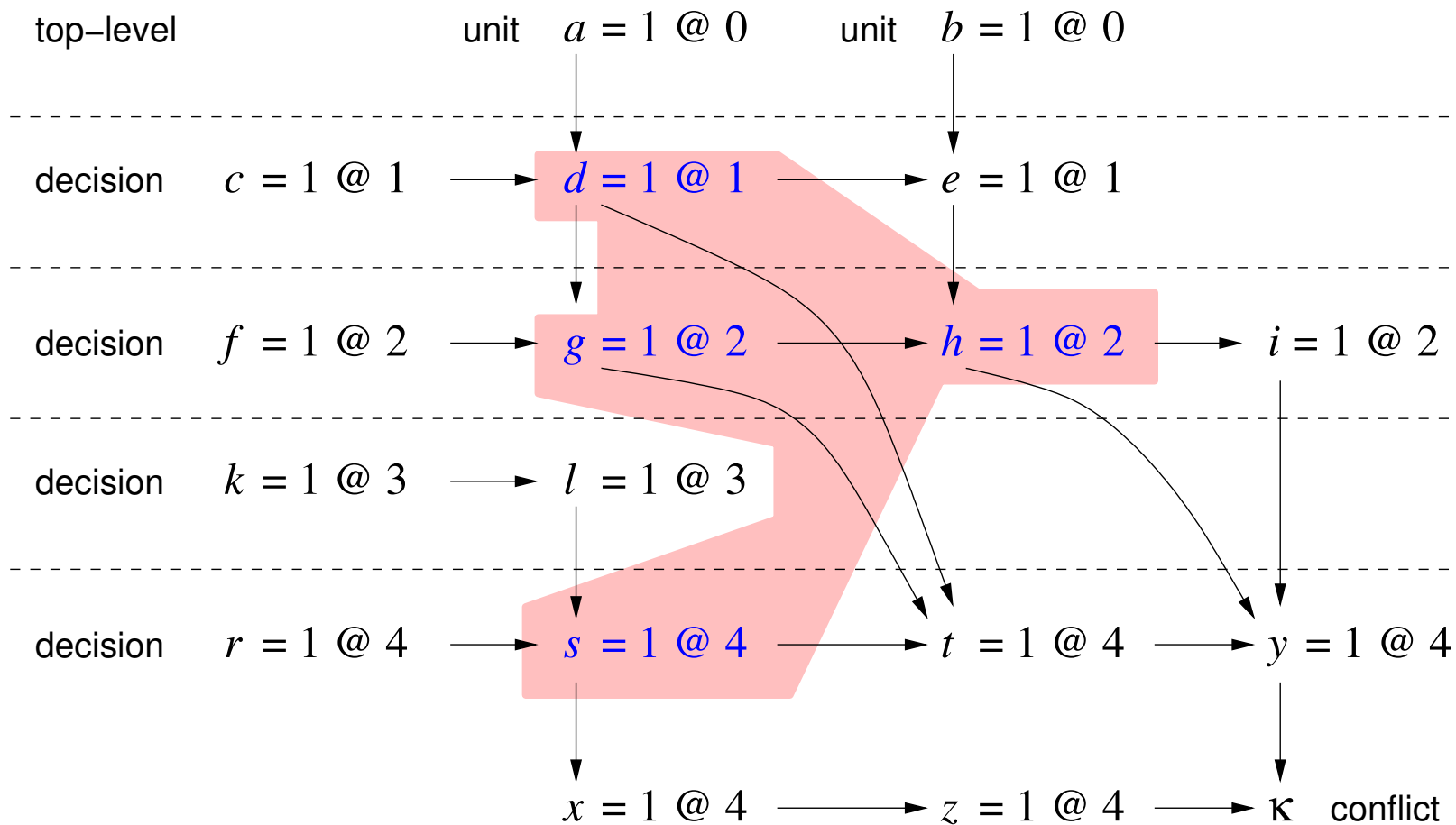


$$(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})$$



$$\frac{(\bar{h} \vee i) \quad (\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h} \vee \bar{i})}{(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h})}$$

self subsuming resolution



$$(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h})$$

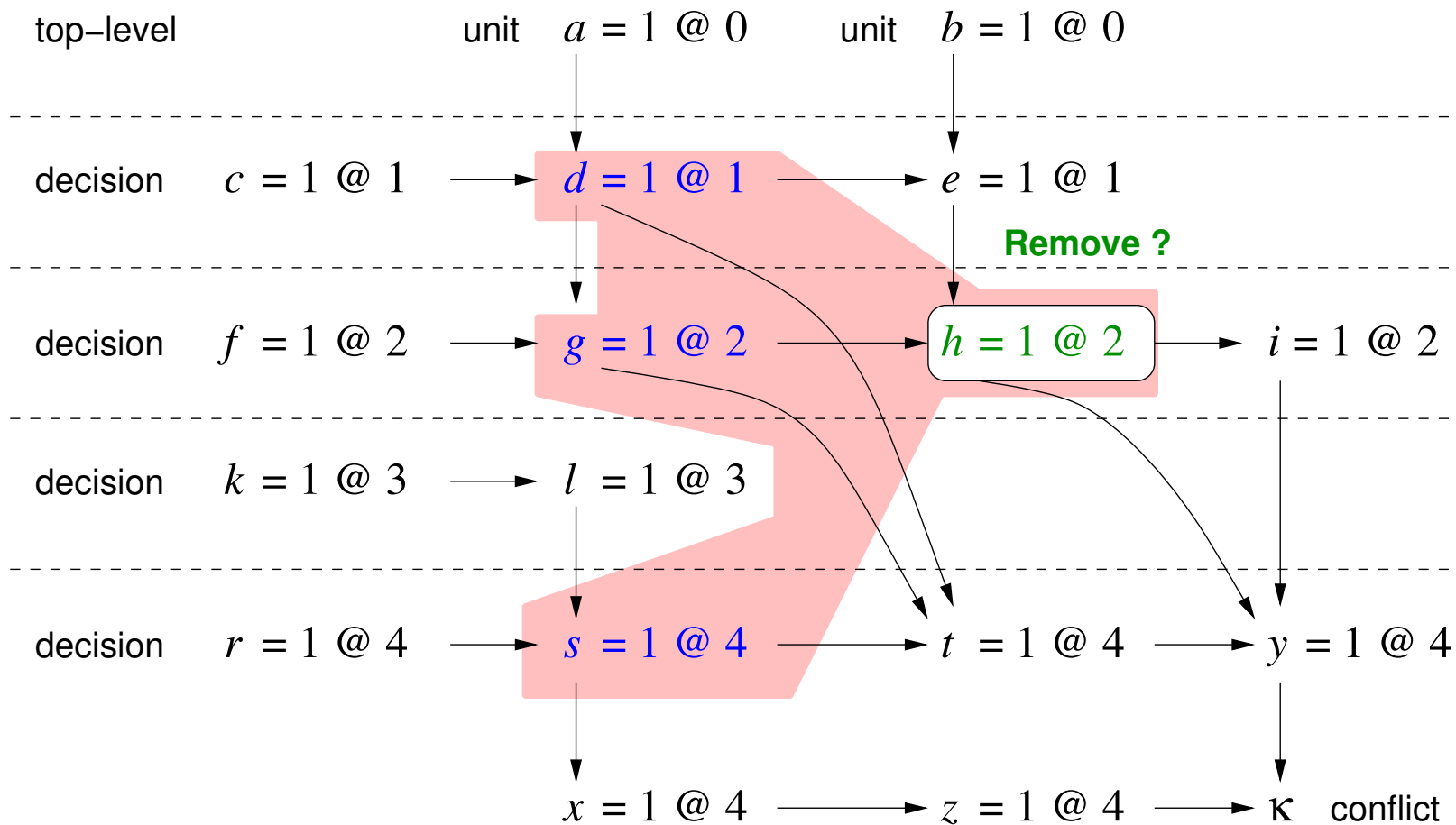
Two step algorithm:

1. mark all variables in 1st UIP clause
2. remove literals with all antecedent literals also marked

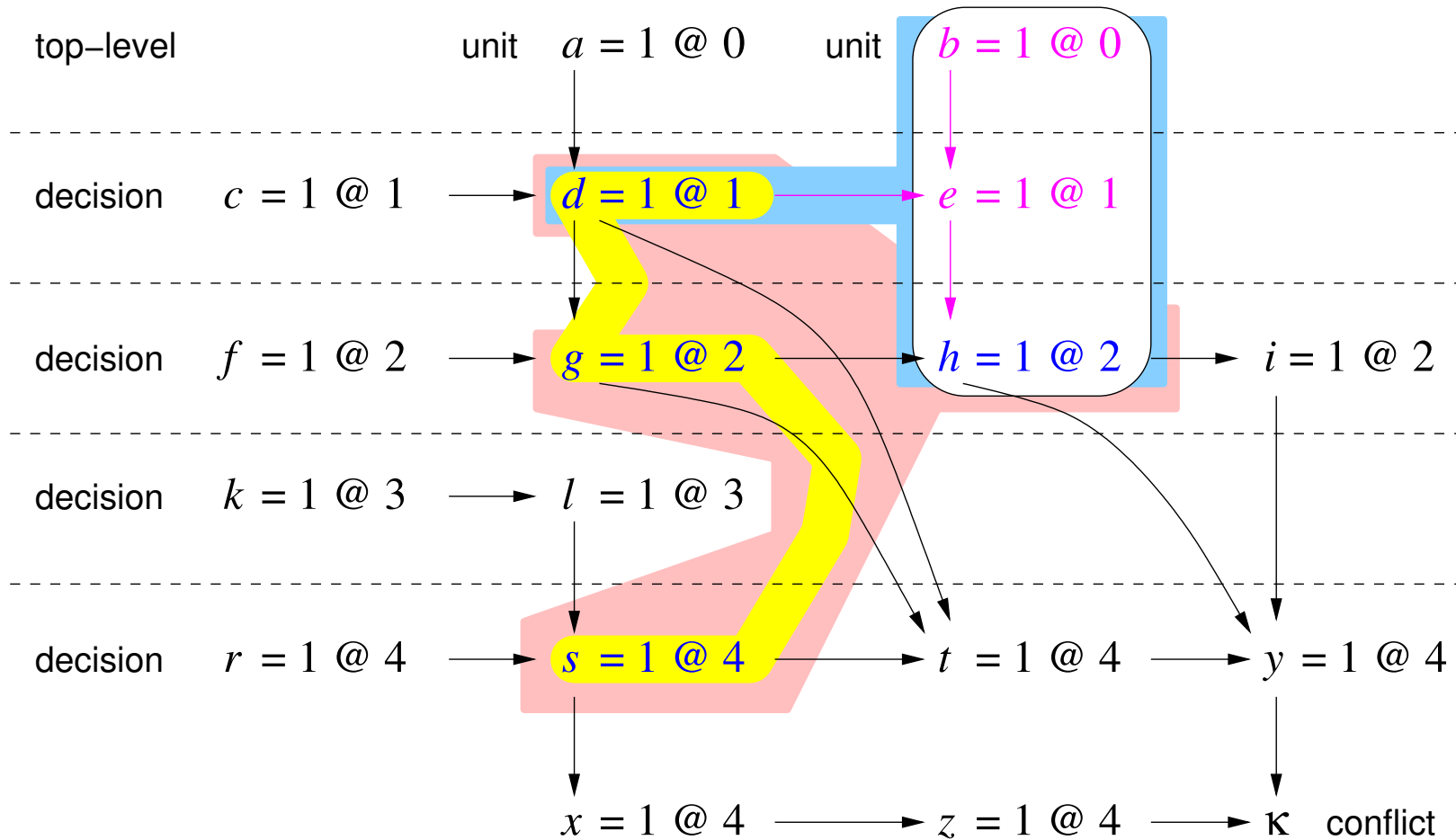
Correctness:

- removal of literals in step 2 are self subsuming resolution steps.
- implication graph is acyclic.

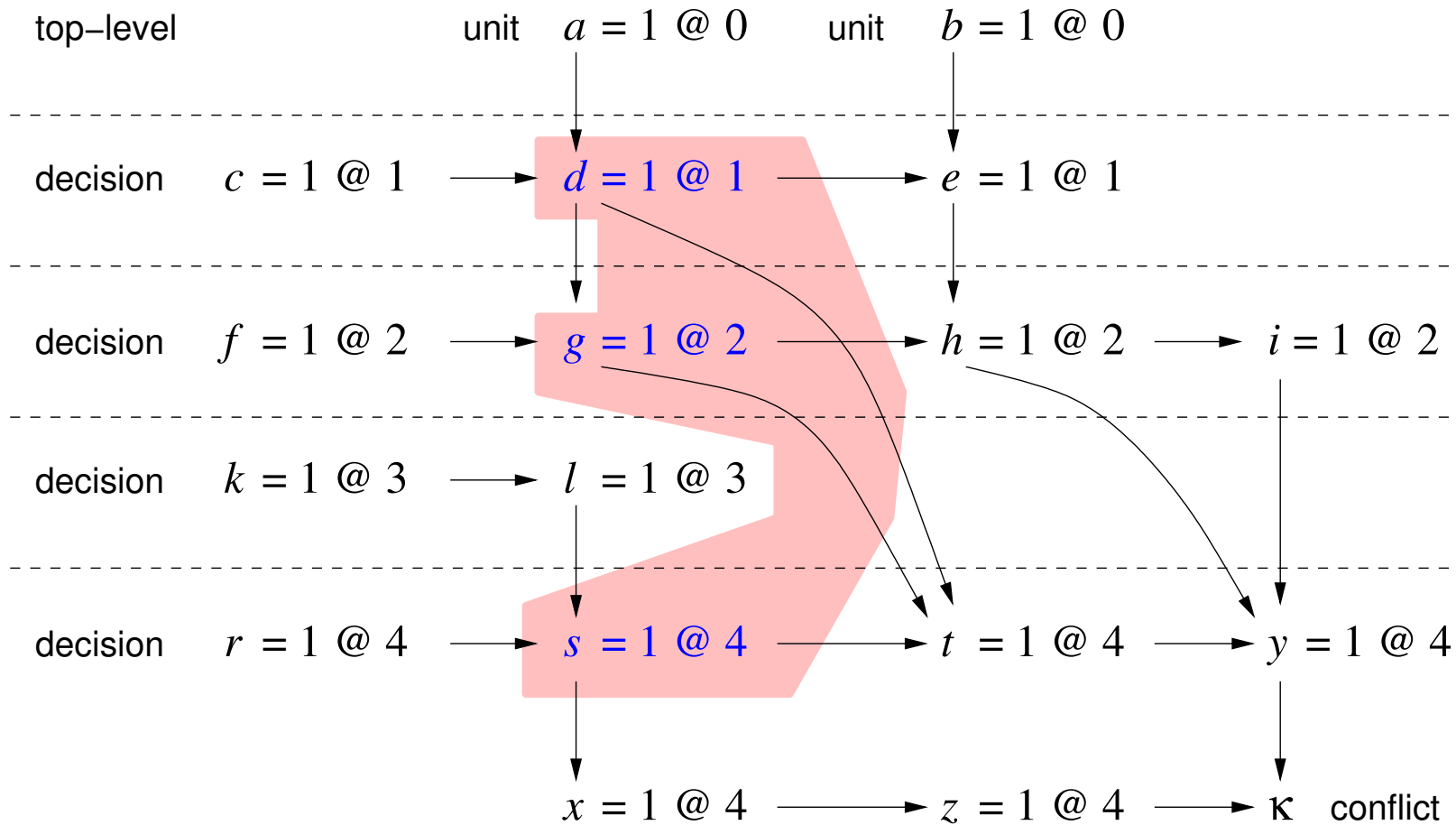
Confluence: produces a unique result.



$$(\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h})$$



$$\begin{array}{c}
 \frac{(\bar{e} \vee \bar{g} \vee h) \quad (\bar{d} \vee \bar{g} \vee \bar{s} \vee \bar{h})}{(\bar{d} \vee \bar{b} \vee e) \quad (\bar{e} \vee \bar{d} \vee \bar{g} \vee \bar{s})} \\
 \frac{(b)}{(\bar{b} \vee \bar{d} \vee \bar{g} \vee \bar{s})} \\
 \hline
 (\bar{d} \vee \bar{g} \vee \bar{s})
 \end{array}$$



$$(\bar{d} \vee \bar{g} \vee \bar{s})$$

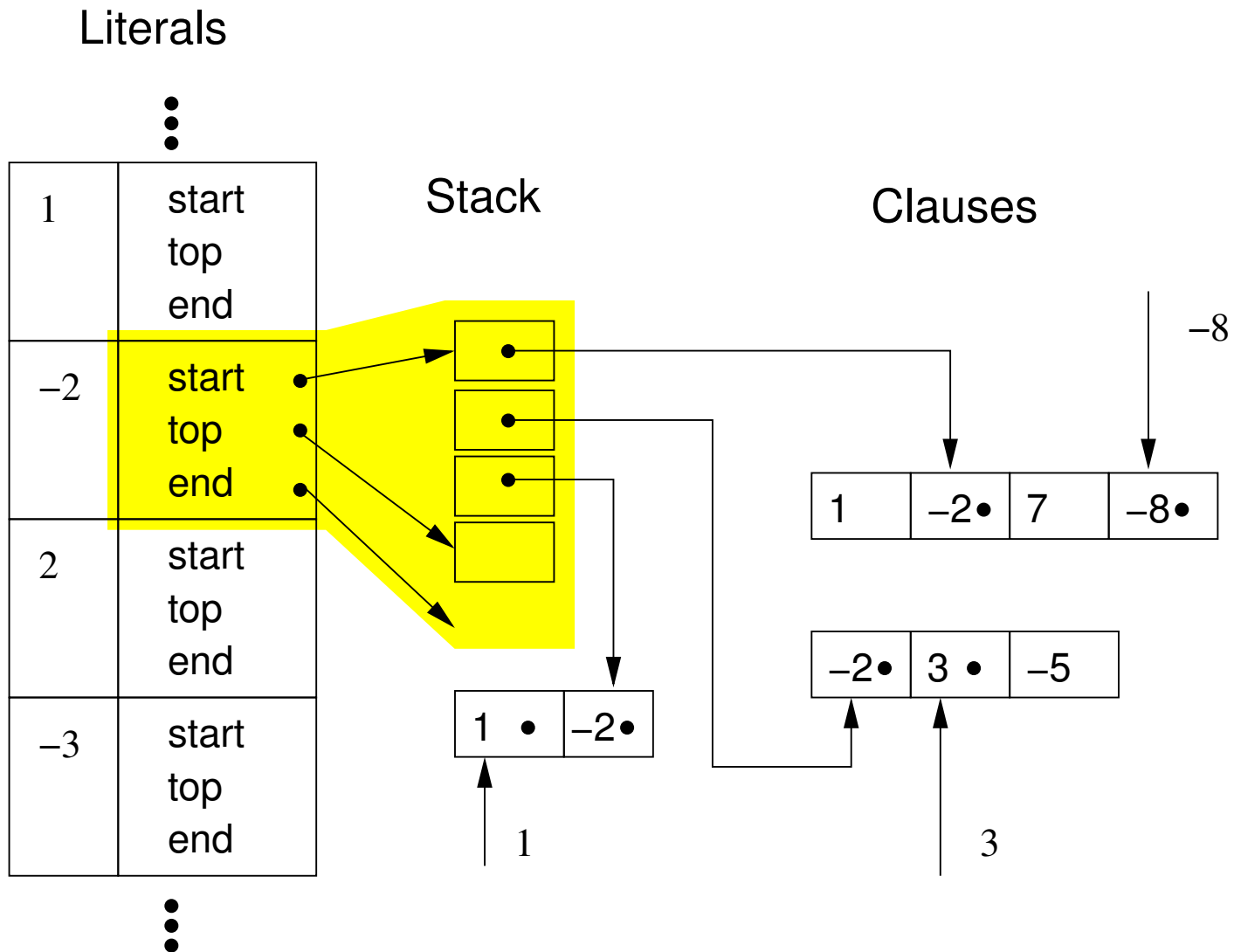
[MiniSAT 1.13]

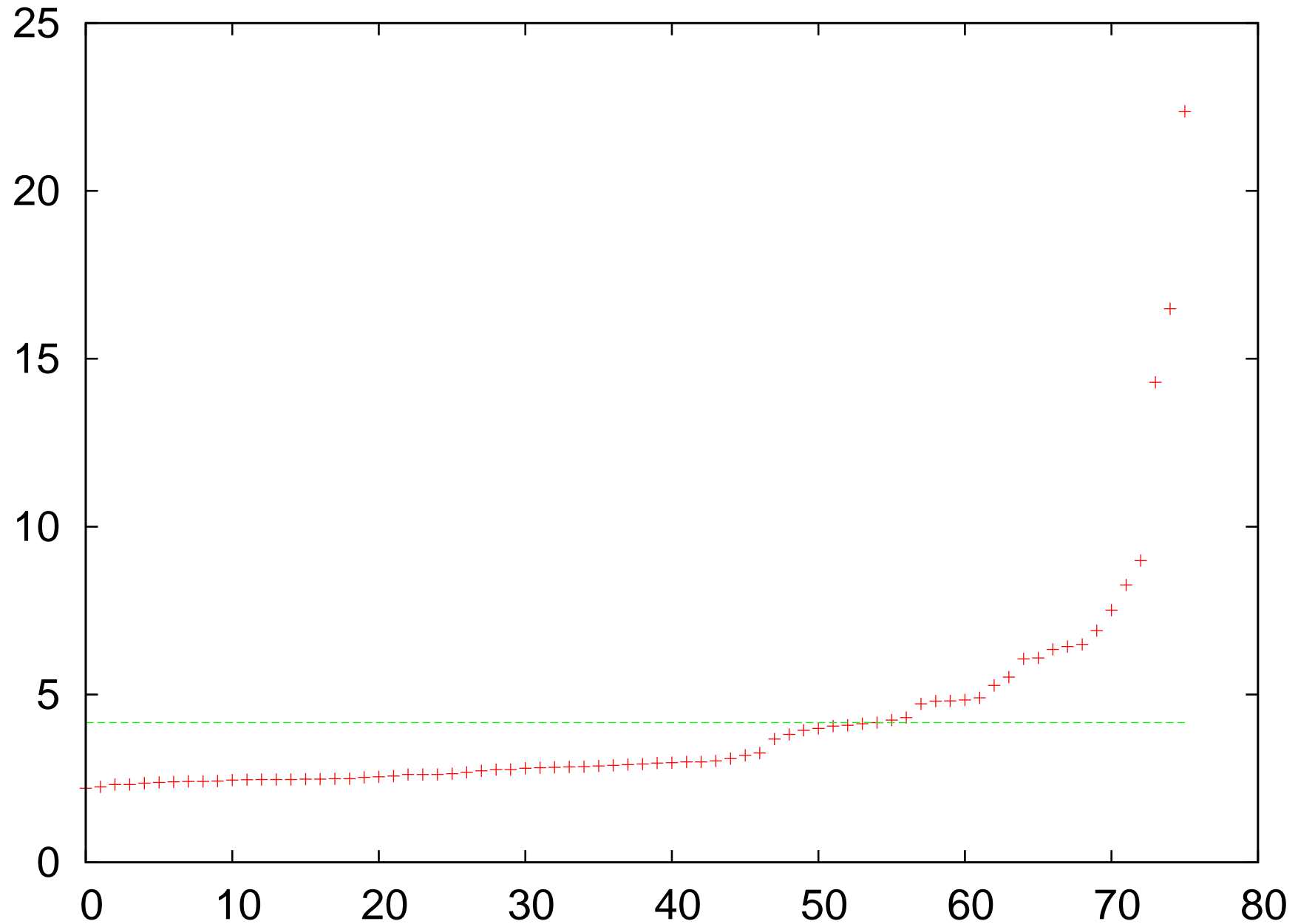
Four step algorithm:

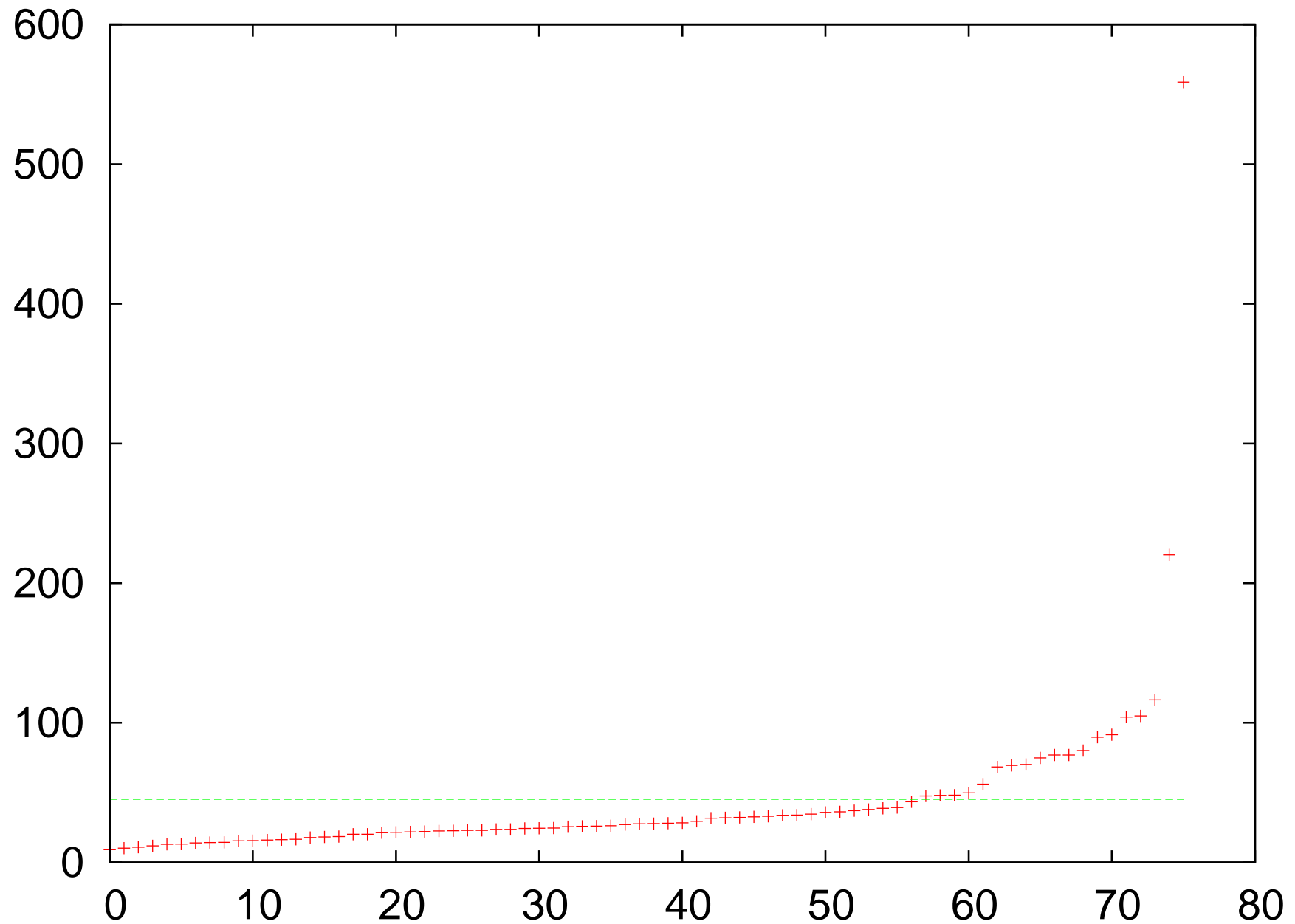
1. mark all variables in 1st UIP clause
2. for each candidate literal: search implication graph
3. start at antecedents of candidate literals
4. if search always terminates at marked literals remove candidate

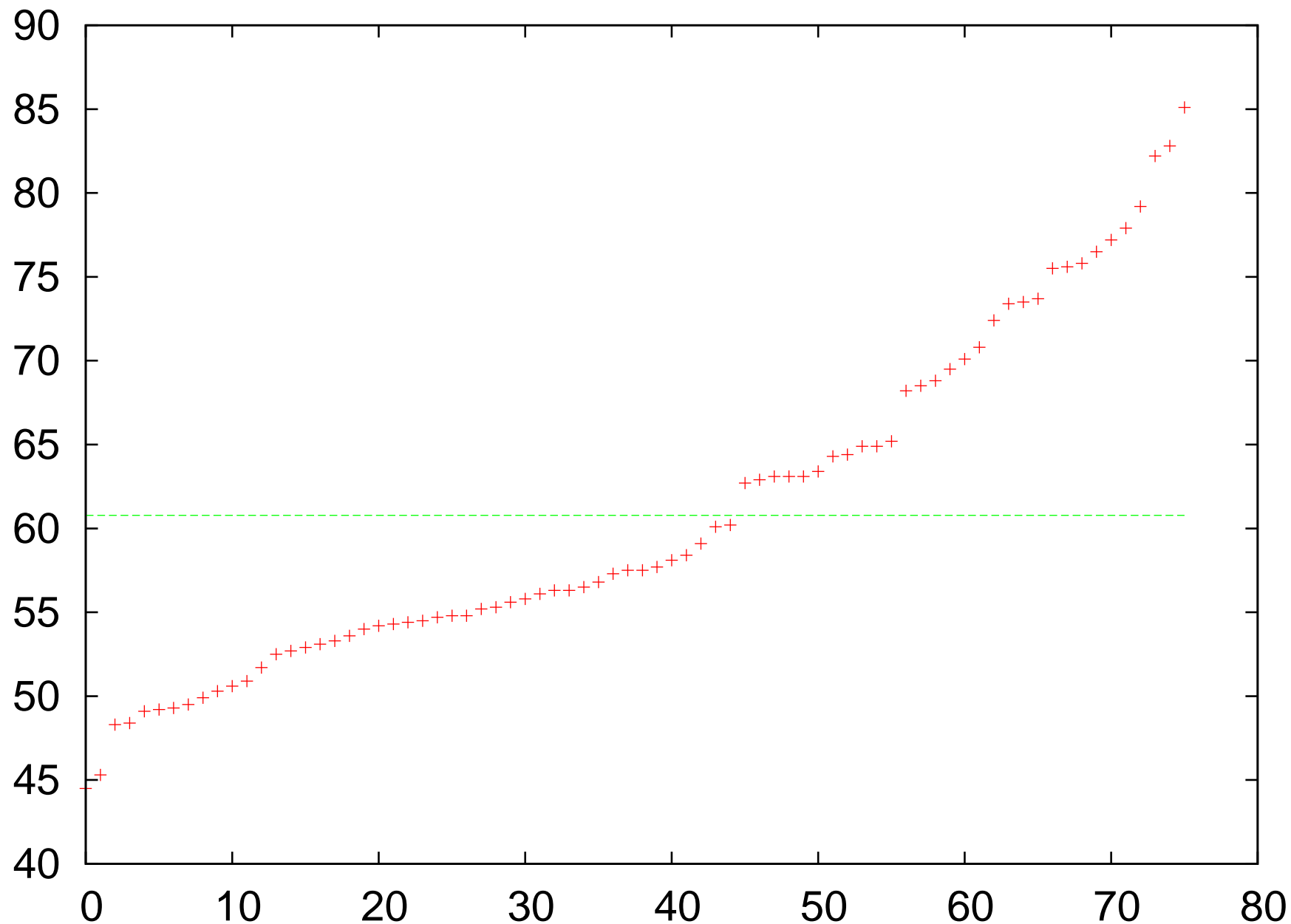
Correctness and **Confluence** as in local version!!!

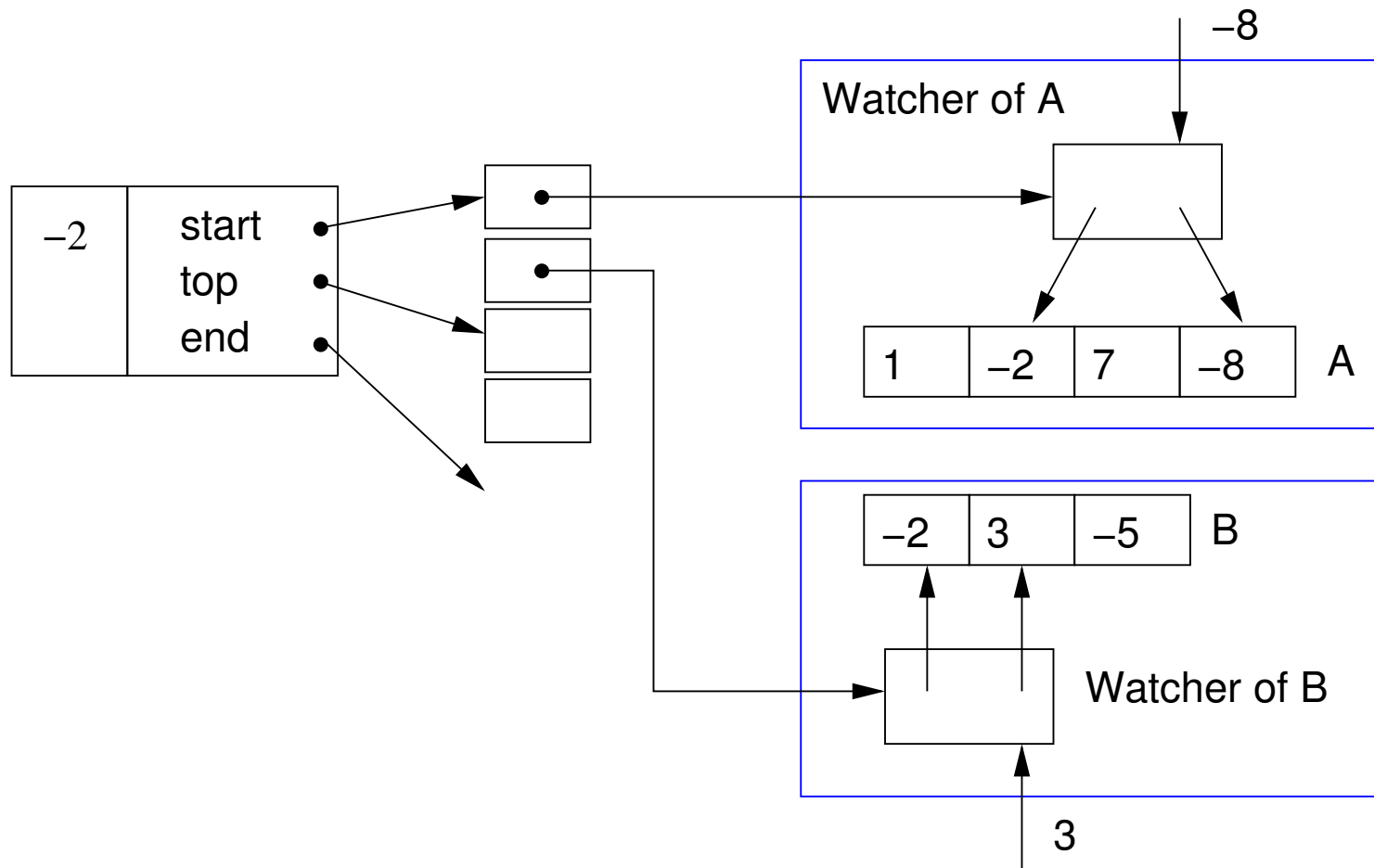
Optimization: terminate early with failure if new decision level is “pulled in”

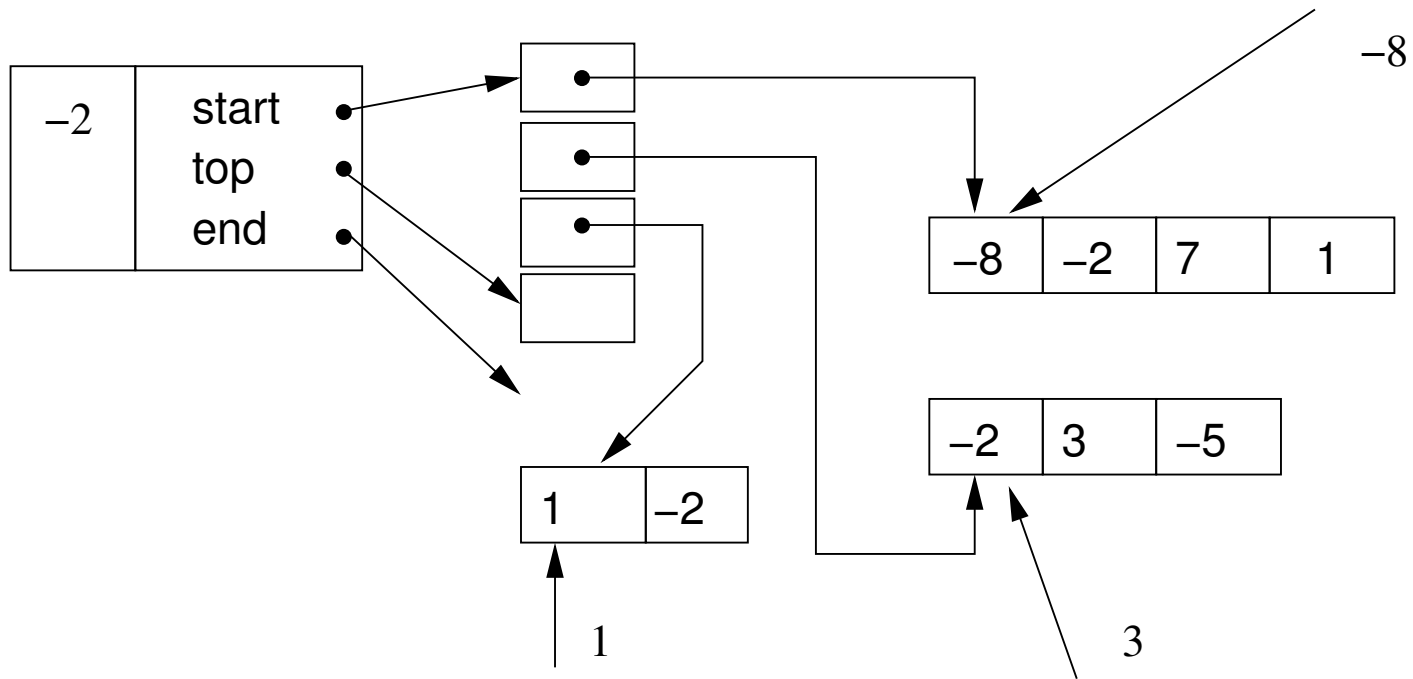




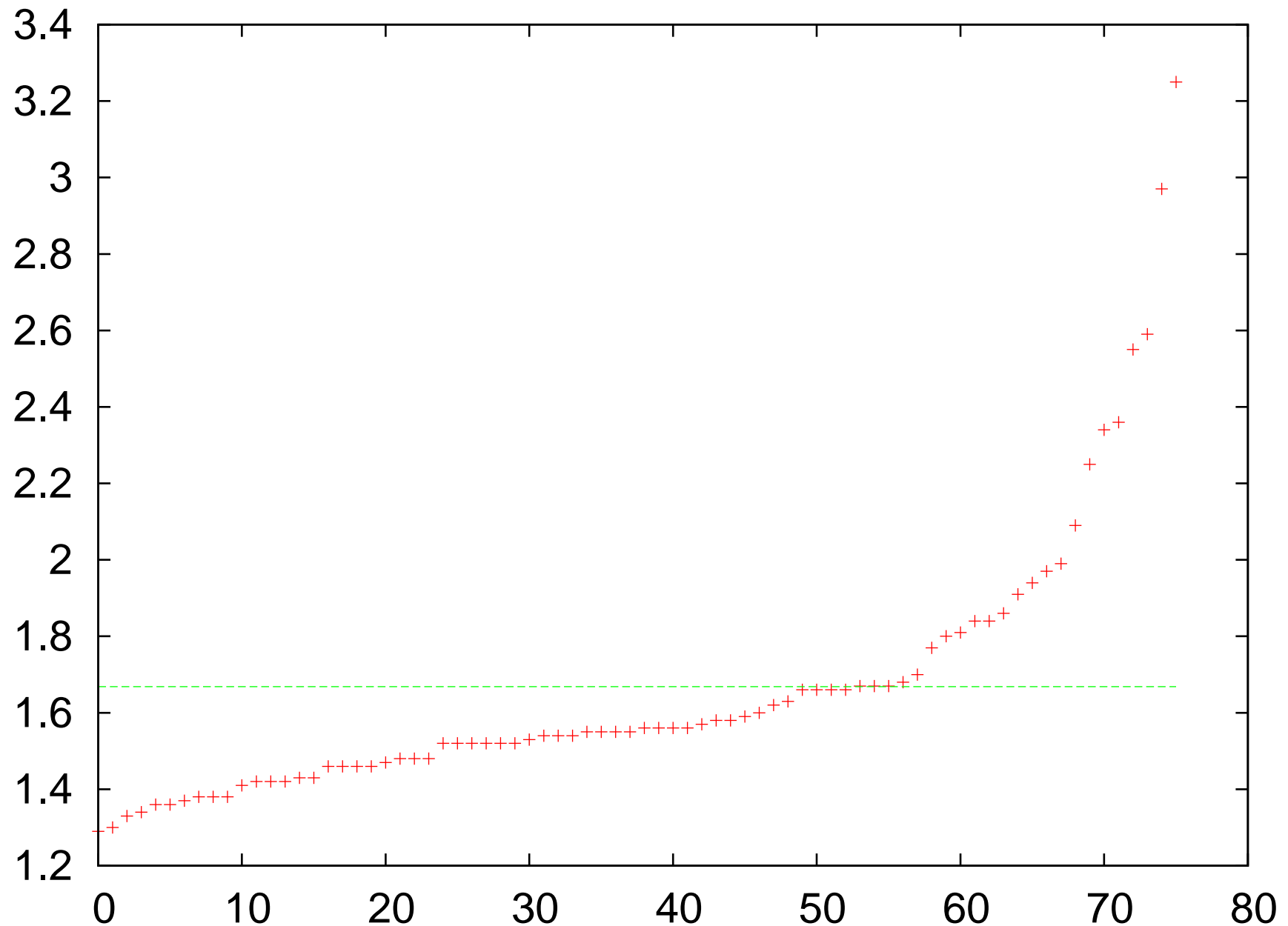


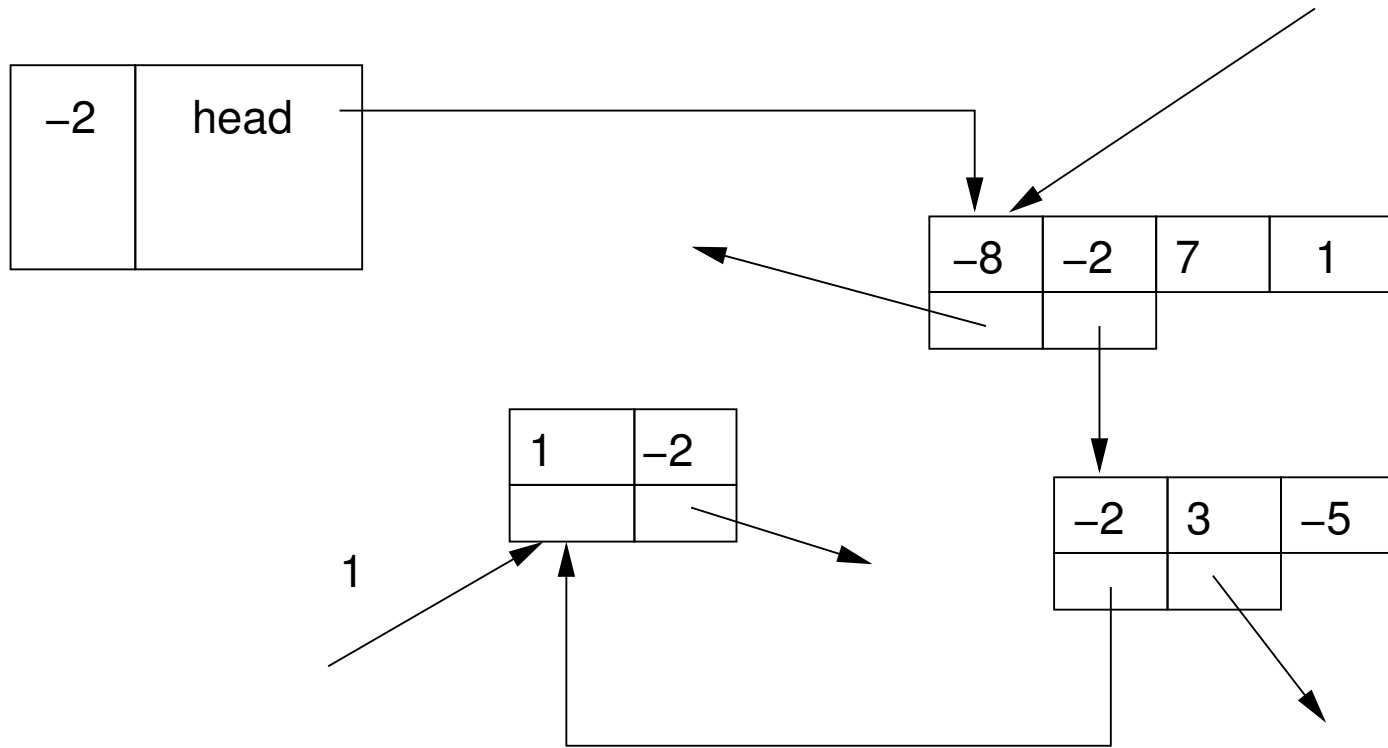






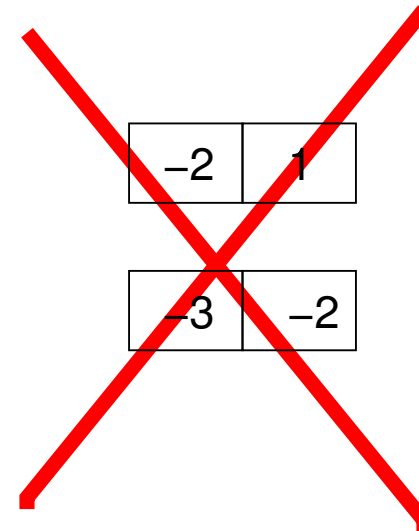
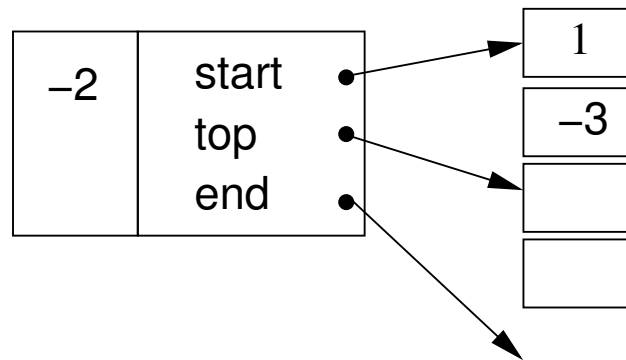
invariant: first two literals are watched

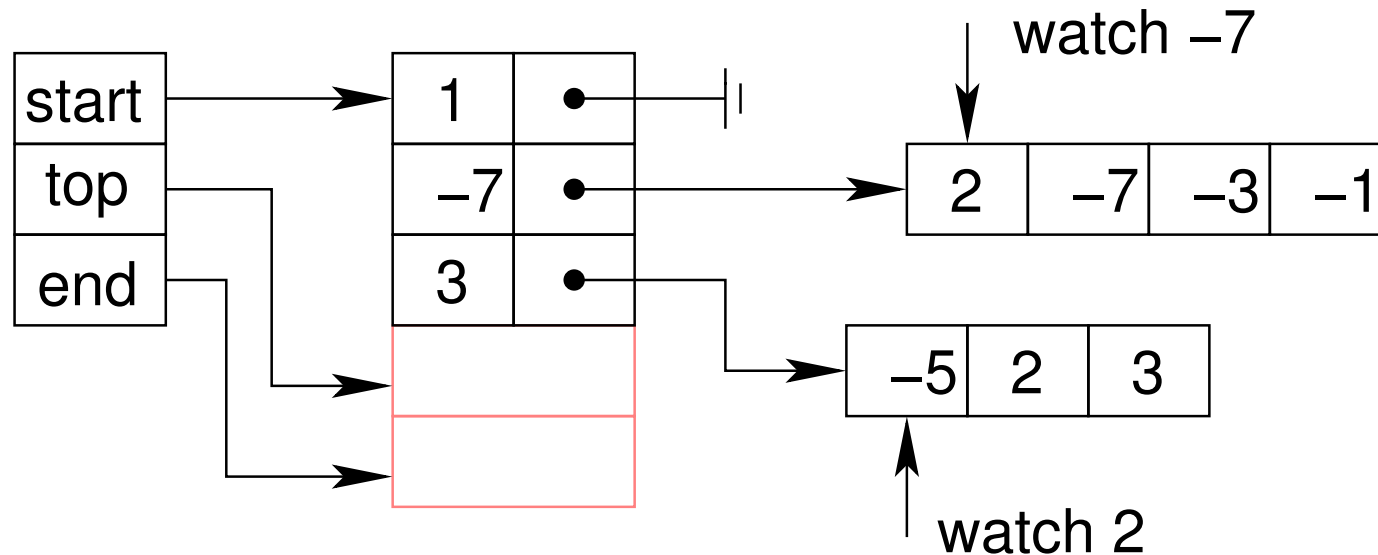




invariant: first two literals are watched

Additional Binary Clause Watcher Stack





observation: often the *other* watched literal satisfies the clause

so cache this literals in watch list to avoid pointer dereference

for binary clause no need to store clause at all

can easily be adjusted for ternary clauses (with full occurrence lists)

LINGELING uses more compact pointer-less variant

we are still working on tracking down the origin before [Freeman'95] [LeBerre'01]

- key technique in look-ahead solvers such as Satz, OKSolver, March
 - failed literal probing at all search nodes
 - used to find the best decision variable and phase
- simple algorithm
 1. assume literal l , propagate (BCP), if this results in conflict, add unit clause $\neg l$
 2. continue with all literals l until *saturation* (nothing changes)
- quadratic to cubic complexity
 - BCP linear in the size of the formula 1st linear factor
 - each variable needs to be tried 2nd linear factor
 - and tried again if some unit has been derived 3rd linear factor

- lifting
 - complete case split: literals implied in all cases become units
 - similar to Stålmarm's method and Recursive Learning [PradhamKunz'94]
- asymmetric branching
 - assume all but one literal of a clause to be false
 - if BCP leads to conflict remove originally remaining unassigned literal
 - implemented for a long time in MiniSAT but switched off by default
- generalizations:
 - vivification [PietteHamadiSais ECAI'08]
 - distillation [JinSomenzi'05][HanSomenzi DAC'07] probably most general (+ tries)

- similar to look-ahead heuristics: polynomially bounded search
 - may be recursively applied (however, is often too expensive)
- Stålmarck's Method
 - works on triplets (intermediate form of the Tseitin transformation):
 $x = (a \wedge b), y = (c \vee d), z = (e \oplus f)$ etc.
 - generalization of BCP to (in)equalities between variables
 - **test rule** splits on the two values of a variable
- Recursive Learning (Kunz & Pradhan)
 - (originally) works on circuit structure (derives implications)
 - splits on different ways to *justify* a certain variable value

1. BCP over (in)equalities: $\frac{x = y \quad z = (x \oplus y)}{z = 0} \quad \frac{x = 0 \quad z = (x \vee y)}{z = y}$ etc.

2. structural rules: $\frac{x = (a \vee b) \quad y = (a \vee b)}{x = y}$ etc.

3. test rule:

$$\frac{\begin{array}{c} \{x = 0\} \cup E \\ \Downarrow \\ E_0 \cup E \end{array} \quad \begin{array}{c} \{x = 1\} \cup E \\ \Downarrow \\ E_1 \cup E \end{array}}{(E_0 \cap E_1) \cup E}$$

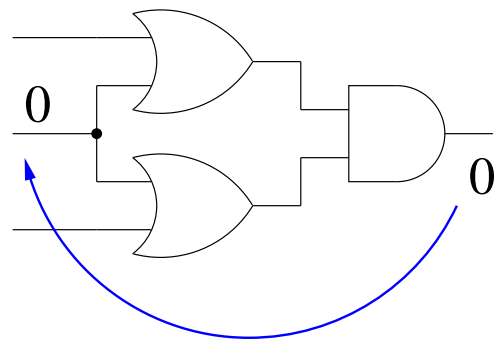
Assume $x = 0$, BCP and derive (in)equalities E_0 .

Then assume $x = 1$, BCP and derive (in)equalities E_1 .

The intersection of E_0 and E_1 contains the (in)equalities valid in *any* case.

- recursive application
 - depth of recursion bounded by number of variables
 - complete procedures (determines satisfiability or unsatisfiability)
 - for a fixed (constant) recursion depth k polynomial!
- k -saturation:
 - apply split rule on recursively up to depth k on all variables
 - 0-saturation: apply all rules except test rule (just BCP: linear)
 - 1-saturation: apply test rule (not recursively) for all variables (until no new (in)equalities can be derived)

- circuits



output 0 implies middle input 0 *indirectly*

- CNF

- for each clause c in the CNF

- * for each literal l in the clause c

- assume l and propagate

- collect set of all implied literals (direct/indirect “implications” of l)

- * intersect these sets of implied literals over all l in c

- * literals in the intersection are implied without any assumption

[DavisPutnam60][Biere SAT'04] [SubbarayanPradhan SAT'04] [EénBiere SAT'05]

- use DP to existentially quantify out variables as in [DavisPutnam60]
- only remove a variable if this does not add (too many) clauses
 - do not count tautological resolvents
 - detect units on-the-fly
- schedule removal attempts with a priority queue [Biere SAT'04] [EénBiere SAT'05]
 - variables ordered by the number of occurrences
- strengthen and remove subsumed clauses (on-the-fly)
(SATeLite [EénBiere SAT'05] and Quantor [Biere SAT'04])

- for each (new or strengthened) clause
 - traverse list of clauses of the least occurring literal in the clause
 - check whether traversed clauses are subsumed or
 - strengthen traversed clauses by self-subsumption [EénBiere SAT'05]
 - use Bloom Filters (as in “bit-state hashing”), aka signatures
- check old clauses being subsumed by new clause: **backward (self) subsumption**
 - new clause (self) subsumes existing clause
 - new clause smaller or equal in size
- check new clause to be subsumed by existing clauses **forward (self) subsumption**
 - can be made more efficient by one-watcher scheme [Zhang-SAT'05]

[AnderssonBjesseCookHanna DAC'02]

also in Oepir SAT solver, this is our reformulation

- for all literals l
 - for all clauses c in which l occurs (with this particular phase)
 - * assume the negation of all the other literals in c , assume l
 - * if BCP does not lead to a conflict continue with next literal in outer loop
 - if all clauses produced a conflict permanently assign $\neg l$

Correctness: Let $c = l \vee d$, assume $\neg d \wedge l$.

If this leads to a conflict $d \vee \neg l$ could be learned (but is not added to the CNF).

Self subsuming resolution with c results in d and c is removed.

If all such cases lead to a conflict, $\neg l$ becomes a pure literal.

Generalization of pure literals.

Given a partial assignment σ .

A clause of a CNF is “touched” by σ if it contains a literal assigned by σ .

A clause of a CNF is “satisfied” by σ if it contains a literal assigned to true by σ .

If all touched clauses are satisfied then σ is an “autarky”.

All clauses touched by an autarky can be removed.

Example: $(-1\ 2)(-1\ 3)(1\ -2\ -3)(2\ 5)\dots$ (more clauses without 1 and 3).

Then $\sigma = \{-1, -3\}$ is an autarky.

one clause $C \in F$ with l

all clauses in F with \bar{l}

fix a CNF F

$$\bar{l} \vee \bar{a} \vee c$$

$$a \vee b \vee l$$

$$\bar{l} \vee \bar{b} \vee d$$

all resolvents of C on l are tautological \Rightarrow **C can be removed**

Proof assume assignment σ satisfies $F \setminus C$ but not C

can be extended to a satisfying assignment of F by flipping value of l

Definition A literal l in a clause C of a CNF F **blocks** C w.r.t. F if for every clause $C' \in F$ with $\bar{l} \in C'$, the resolvent $(C \setminus \{l\}) \cup (C' \setminus \{\bar{l}\})$ obtained from resolving C and C' on l is a tautology.

Definition [Blocked Clause] A clause is **blocked** if has a literal that blocks it.

Definition [Blocked Literal] A literal is **blocked** if it blocks a clause.

Example

$$(a \vee b) \wedge (a \vee \bar{b} \vee \bar{c}) \wedge (\bar{a} \vee c)$$

only **first clause** is not blocked.

second clause contains two blocked literals: a and \bar{c} .

literal c in the **last clause** is blocked.

after removing either $(a \vee \bar{b} \vee \bar{c})$ or $(\bar{a} \vee c)$, the clause $(a \vee b)$ becomes blocked

actually all clauses can be removed

[JärvisaloBiereHeule-TACAS'10]

COI Cone-of-Influence reduction

MIR Monotone-Input-Reduction

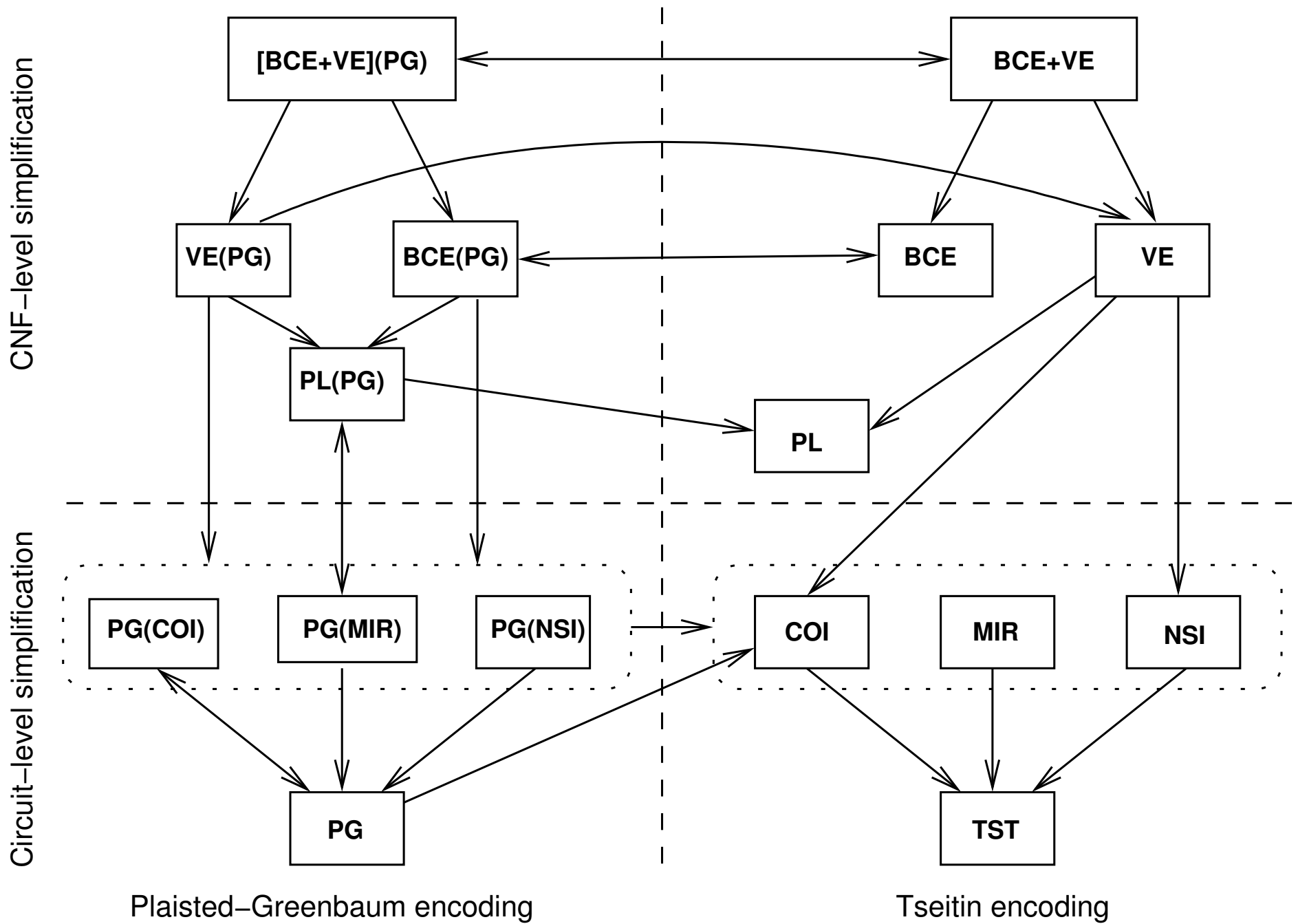
NSI Non-Shared Inputs reduction

PG Plaisted-Greenbaum polarity based encoding

TST standard Tseitin encoding

VE Variable-Elimination as in DP / Quantor / SATeLite

BCE Blocked-Clause-Elimination



PrecoSAT [Biere'09], Lingeling [Biere'10], now also in CryptoMiniSAT (Mate Soos)

- preprocessing can be extremely beneficial
 - most SAT competition solvers use variable elimination (VE) [EénBiere SAT'05]
 - equivalence / XOR reasoning
 - probing / failed literal preprocessing / hyper binary resolution
 - however, even though polynomial, **can not be run until completion**
- simple idea to benefit from full preprocessing without penalty
 - **“preempt” preprocessors** after some time
 - **resume preprocessing** between restarts
 - limit preprocessing time in relation to search time

- allows to use *costly* preprocessors
 - without increasing run-time “much” in the worst-case
 - still useful for benchmarks where these costly techniques help
 - good examples: probing and distillation even VE can be costly
- additional benefit:
 - makes units / equivalences learned in search available to preprocessing
 - particularly interesting if preprocessing simulates encoding optimizations
- danger of hiding “bad” implementation though ...
- ... and hard(er) to debug

equivalent literal substitution find strongly connected components in binary implication graph, replace equivalent literals by representatives

boolean ring reasoning extract XORs, then Gaussian elimination etc.

hyper-binary resolution focus on producing binary resolvents

hidden/asymmetric tautology elimination discover redundant clauses through probing

covered clause elimination use covered literals in probing for redundant clauses

unhiding randomized algorithm (one phase linear) for clause removal and strengthening